

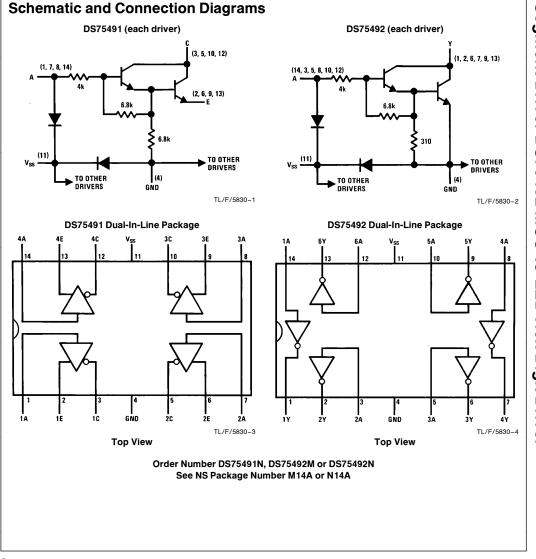
DS75491 MOS-to-LED Quad Segment Driver DS75492 MOS-to-LED Hex Digit Driver

General Description

The DS75491 and DS75492 are interface circuits designed to be used in conjunction with MOS integrated circuits and common-cathode LEDs in serially addressed multi-digit displays. The number of drivers required for this time-multiplexed system is minimized as a result of the segment-address-and-digit-scan method of LED drive.

Features

- 50 mA source or sink capability per driver (DS75491)
- 250 mA sink capability per driver (DS75492)
- MOS compatability (low input current)
- Low standby power
- High-gain Darlington circuits



DS75491 MOS-to-LED Quad Segment Driver DS75492 MOS-to-LED Hex Digit Driver

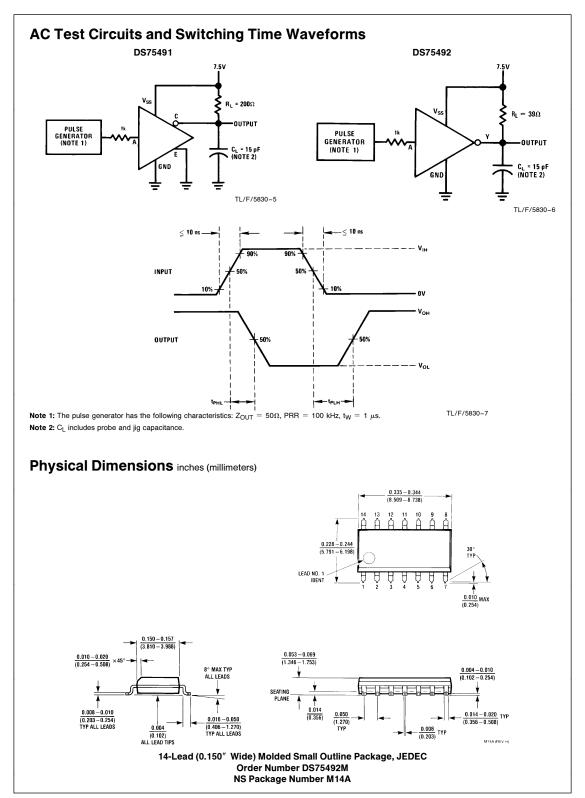
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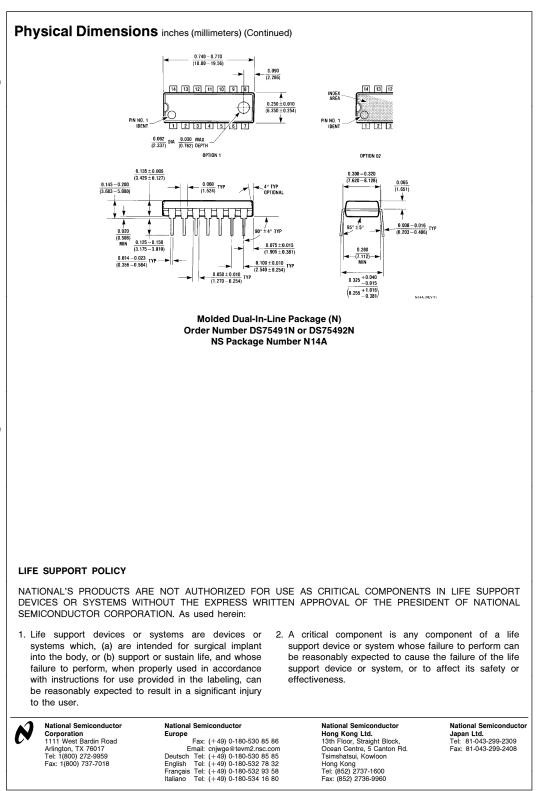
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RRD-B30M105/Printed in U. S. A.

lf Milita please	Diute Maximum Rati ary/Aerospace specified dev contact the National Sem	required, or Sales		tinuous Total Dissipation		75491 0 mW	600	'5492 mW	
Office/	Distributors for availability an	ations. DS75492	Operating Temperature Range Storage Temperature Range		0°C to +70°C -65°C to +150°C				
Input Voltage Range (Note 4) -5V to				Lead Temp. (Soldering, 10 sec)		300°C 300°			
Collector Output Voltage (Note 5) 10V			10V	Maximum Pov				•••	
	or Output to Input Voltage	10V	10V	at 25°C					
	to Ground Voltage ($V_{I} \ge 5V$)	10V		Molded Pac	ckage package 9.66 mW/°C a		7 mW*	1280	mW.
	to Input Voltage at V _{SS} Terminal with Respect	5V			package 10.24 mW/°C a				
-	y Other Device Terminal	10V	10V						
Collecto	or Output Current								
			250 mA						
		200 mA	600 mA						
	trical Characteristic	S V _{SS} =	10V (Notes 2 ar				-		
Symbol	Parameter			Conditions		Min	Тур	Max	Un
DS75491					T 0500				
V _{CE} ON	"ON" State Collector Emitter Voltage "OFF" State Collector Current		$V_{E} = 5V, I_{C} = V_{C} = 10V,$ $V_{E} = 0V$	through 1 k Ω , = 50 mA			0.9	1.2	
					$T_A = 0-70^{\circ}C$			1.5	\ \
				$I_{IN} = 40 \ \mu A$	= 40 μΑ			100	μ
				$V_{IN} = 0.7V$				100	μ
I	Input Current at Maximum Input Voltage		$V_{IN} = 10V, V$	$V_{IN} = 10V, V_E = 0V, I_C = 20 \text{ mA}$			2.2	3.3	m
E	Emitter Reverse Current		$V_{IN} = 0V, V_E = 5V, I_C = 0 \text{ mA}$					100	μ
SS	Current Into V _{SS} Terminal							1	m
DS75492	2								-
V _{OL}	Low Level Output Voltage		Input = 6.5V	through 1 k Ω ,	$T_A = 25^{\circ}C$		0.9	1.2	\
			I _{OUT} = 250 r	mA	$T_A = 0-70^{\circ}C$			1.5	\ \
I _{OH}	High Level Output Current		V _{OH} = 10V	$I_{IN} = 40 \ \mu A$	·			200	μ
				$V_{IN} = 0.5V$				200	μ
li	Input Current at Maximum Input Voltage		$V_{\rm IN} = 10V, I_{\rm OL} = 20 {\rm mA}$				2.2	3.3	m
ss	Current Into V _{SS} Terminal							1	m
	ching Characteristi		7.5V T 25	°C				-	
Symbol	Para	7.5 V , TA 25		Conditions	Min	Тур	Max	Ur	
DS75491				·					
PLH	Propagation Delay Time, Low-	el Output (Colle	ector) V _{IH} =	4.5V, $V_{E} = 0V$,		100		n	
PHL	Propagation Delay Time, High-	el Output (Collector) R _L = 2		$200\Omega, C_{L} = 15 \text{pF}$		20		r	
DS75492				•					
PLH	Propagation Delay Time, Low-	el Output	V _{IH} =	$V_{IH} = 7.5V, R_L = 39\Omega,$		300		n	
PHL	Propagation Delay Time, High-	el Output CL		15 pF		30		n	
Note 1: "/	Absolute Maximum Ratings" are those vanot meant to imply that the devices show								
Note 3: A	Inless otherwise specified min/max limits Il currents into device pins shown as pos r min on absolute value basis. 'he input is the only device terminal whic	itive, out of de	vice pins as negativ	ve, all voltages refe				All values	shov

Note 5: Voltage values are with respect to network ground terminal unless otherwise noted.





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National Semiconductor was acquired by Texas Instruments.

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This file is the datasheet for the following electronic components:

- DS75492M http://www.ti.com/product/ds75492m?HQS=TI-null-null-dscatalog-df-pf-null-wwe
- DS75492N http://www.ti.com/product/ds75492n?HQS=TI-null-null-dscatalog-df-pf-null-wwe
- DS75491N http://www.ti.com/product/ds75491n?HQS=TI-null-null-dscatalog-df-pf-null-wwe