

MITSUBISHI IGBT MODULES
CM900DU-24NF

HIGH POWER SWITCHING USE

CM900DU-24NF



- IC 900A
- VCES 1200V
- Insulated Type
- 2-elements in a pack

APPLICATION

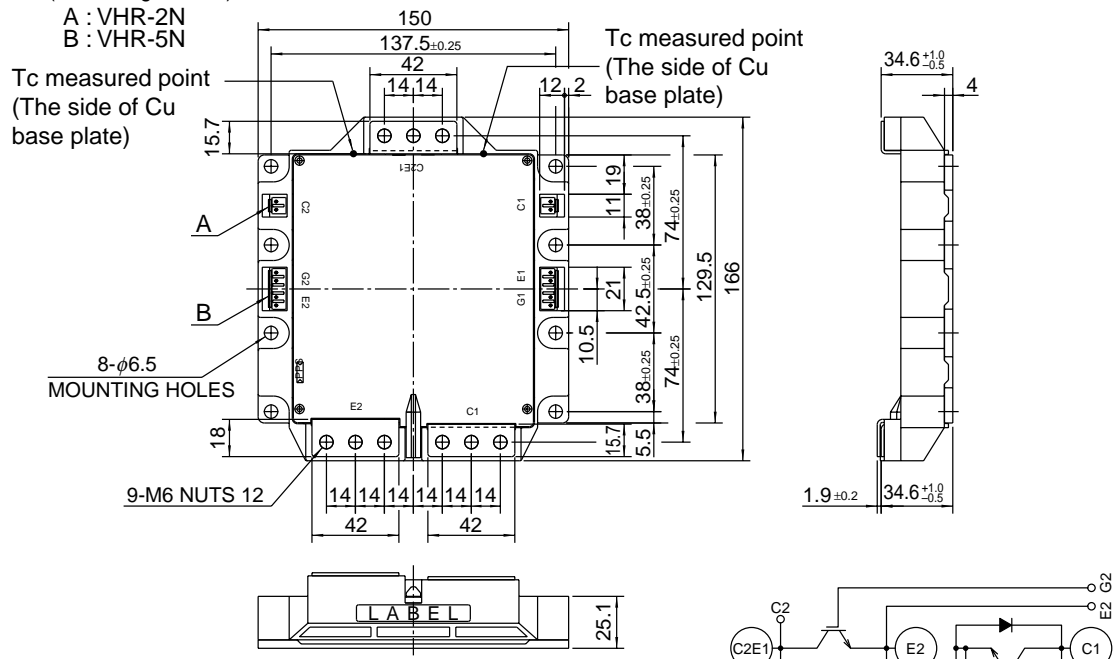
UPS & General purpose inverters, etc

OUTLINE DRAWING & CIRCUIT DIAGRAM

Dimensions in mm

A,B HOUSING Type
 (J. S. T. Mfg. Co. Ltd)

A : VHR-2N
 B : VHR-5N



CIRCUIT DIAGRAM

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MAXIMUM RATINGS (T_j = 25°C)

Symbol	Parameter	Conditions	Ratings	Unit
V _{CE}	Collector-emitter voltage	G-E Short	1200	V
V _{GE}	Gate-emitter voltage	C-E Short	±20	V
I _C	Collector current	T _c = 25°C	900	A
I _{CM}		Pulse (Note 2)	1800	
I _E (Note 1)	Emitter current	T _c = 25°C	900	A
I _{EM} (Note 1)		Pulse (Note 2)	1800	
P _C (Note 3)	Maximum collector dissipation	T _c = 25°C	2550	W
T _j	Junction temperature		-40 ~ +150	°C
T _{stg}	Storage temperature		-40 ~ +125	°C
V _{iso}	Isolation voltage	Main terminal to base plate, AC 1 min.	2500	V
—	Torque strength	Main Terminal M6	3.5 ~ 4.5	N • m
		Mounting holes M6	3.5 ~ 4.5	N • m
—	Weight	Typical value	1400	g

ELECTRICAL CHARACTERISTICS (T_j = 25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
I _{CE}	Collector cutoff current	V _{CE} = V _{CE} , V _{GE} = 0V	—	—	1	mA
V _{GE(th)}	Gate-emitter threshold voltage	I _C = 90mA, V _{CE} = 10V	6	7	8	V
I _{GE}	Gate leakage current	V _{GE} = V _{CE} , V _{CE} = 0V	—	—	0.5	µA
V _{CE(sat)}	Collector-emitter saturation voltage	T _j = 25°C	—	1.8	2.5	V
		T _j = 125°C	—	2.0	—	
R _(lead)	Module lead resistance	I _C = 900A, terminal-chip	—	0.143	—	mΩ
C _{ies}	Input capacitance	V _{CE} = 10V V _{GE} = 0V	—	—	140	nF
C _{oes}	Output capacitance		—	—	16	
C _{res}	Reverse transfer capacitance		—	—	3	
Q _G	Total gate charge	V _{CC} = 600V, I _C = 900A, V _{GE} = 15V	—	4800	—	nC
t _{d(on)}	Turn-on delay time	V _{CC} = 600V, I _C = 900A V _{GE1} = V _{GE2} = 15V R _G = 0.35Ω, Inductive load switching operation	—	—	600	ns
t _r	Turn-on rise time		—	—	200	
t _{d(off)}	Turn-off delay time		—	—	800	
t _f	Turn-off fall time		—	—	300	
t _{rr} (Note 1)	Reverse recovery time	I _E = 900A	—	—	500	ns
Q _{rr} (Note 1)	Reverse recovery charge		—	50	—	µC
V _{EC} (Note 1)	Emitter-collector voltage	I _E = 900A, V _{GE} = 0V	—	—	3.4	V
R _{th(j-c)Q}	Thermal resistance ^{*1}	IGBT part (1/2 module)	—	—	0.049	°C/W
R _{th(j-c)R}		FWDi part (1/2 module)	—	—	0.078	
R _{th(c-f)}	Contact thermal resistance	Case to fin, Thermal compound applied ^{*2} (1/2 module)	—	0.016	—	
R _{th(j-c')Q}	Thermal resistance	T _c measured point is just under the chips (IGBT part)	—	—	0.021 ^{*3}	
R _{th(j-c')R}		T _c measured point is just under the chips (FWDi part)	—	—	0.034 ^{*3}	
R _G	External gate resistance		0.35	—	2.2	Ω

Note 1. I_E, V_{EC}, t_{rr}, Q_{rr}, di_e/dt represent characteristics of the anti-parallel, emitter to collector free-wheel diode. (FWDi).

2. Pulse width and repetition rate should be such that the device junction temp. (T_j) does not exceed T_{jmax} rating.

3. Junction temperature (T_j) should not increase beyond 150°C.

*1 : T_c measured point is indicated in OUTLINE DRAWING.

*2 : Typical value is measured by using Shin-etsu Silicone "G-746".

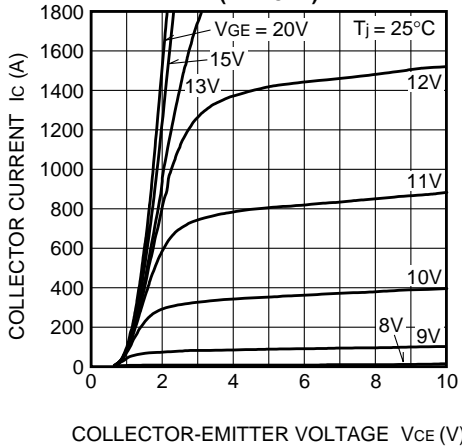
*3 : If you use this value, R_{th(f-a)} should be measured just under the chips.

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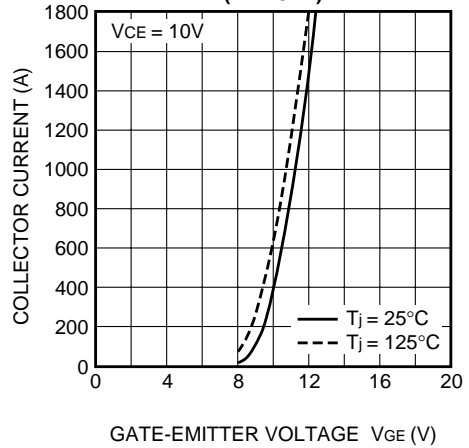
HIGH POWER SWITCHING USE

PERFORMANCE CURVES

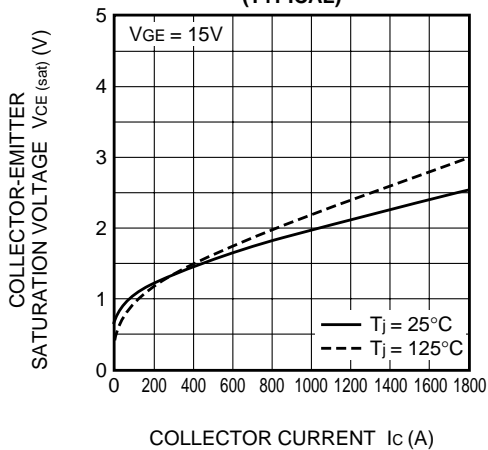
OUTPUT CHARACTERISTICS (TYPICAL)



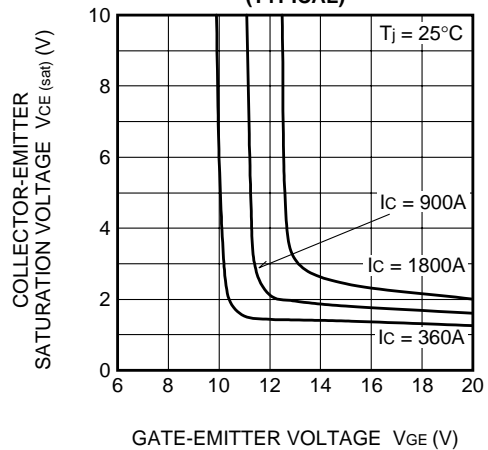
TRANSFER CHARACTERISTICS (TYPICAL)



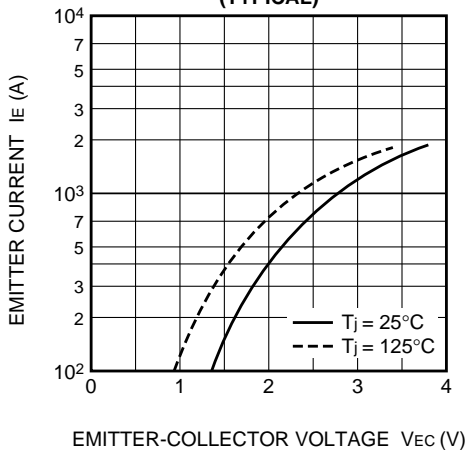
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



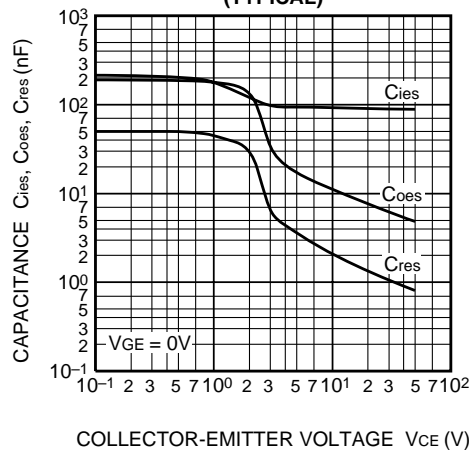
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)



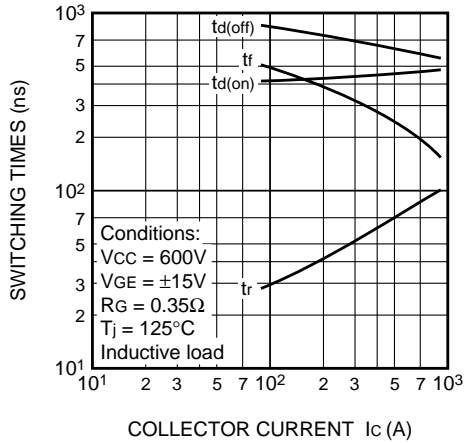
CAPACITANCE-VCE CHARACTERISTICS (TYPICAL)



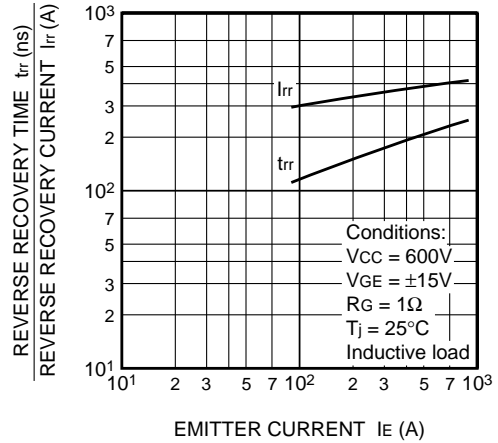
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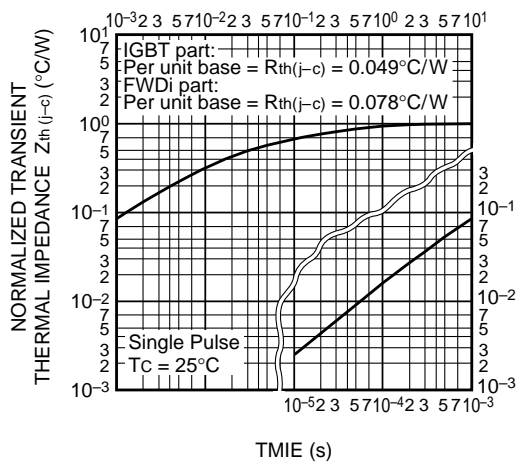
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



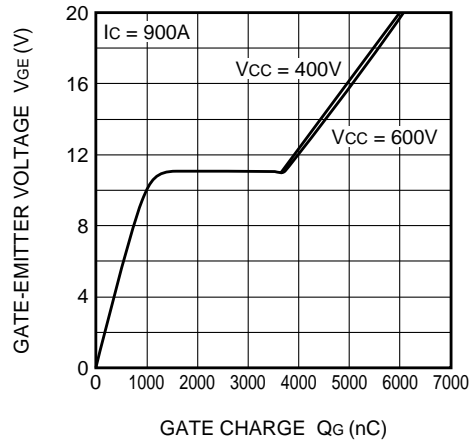
REVERSE RECOVERY CHARACTERISTICS OF FREE-WHEEL DIODE (TYPICAL)



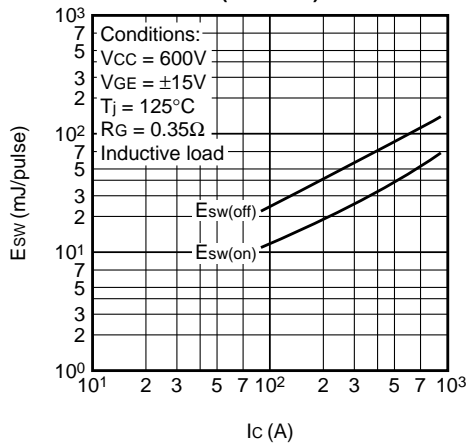
TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (IGBT part & FWDi part)



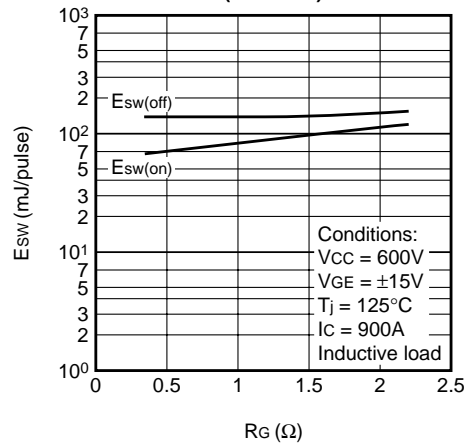
GATE CHARGE CHARACTERISTICS (TYPICAL)



Ic-Esw (TYPICAL)



Rg-Esw (TYPICAL)



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