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FDS6614A

N-Channel Logic Level PowerTrench® MOSFET

General Description

This N-Channel Logic Level MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench process that has been especially tailored to minimize on-state resistance and yet maintain superior switching performance.

These devices are well suited for low voltage and battery powered applications where low in-line power loss and fast switching are required.

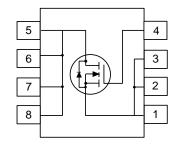
Applications

- DC/DC converter
- · Load switch
- Motor drives

Features

- 9.3 A, 30 V. $R_{DS(on)}$ = 0.018 Ω @ V_{GS} = 10 V $R_{DS(on)}$ = 0.025 Ω @ V_{GS} = 4.5 V.
- Low gate charge (12nC typical).
- · Fast switching speed.
- High performance trench technology for extremely low $R_{\mbox{\scriptsize DS(on)}}.$
- High power and current handling capability.





Absolute Maximum Ratings TA=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V _{DSS}	Drain-Source Voltage		30	V
V _{GSS}	Gate-Source Voltage		± 20	V
I _D	Drain Current - Continuous	(Note 1a)	9.3	Α
	- Pulsed		40	
P _D	Power Dissipation for Single Operation	(Note 1a)	2.5	W
		(Note 1b)	1.2	
		(Note 1c)	1.0	
T _J , T _{stg}	Operating and Storage Junction Temperature Range		-55 to +150	°C

Thermal Characteristics

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1a)	50	°C/W
R _{θJC}	Thermal Resistance, Junction-to-Case	(Note 1)	25	°C/W

Package Marking and Ordering Information

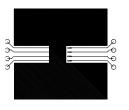
Device Marking	Device	Reel Size	Tape width	Quantity	
FDS6614A FDS6614A		13"	12mm	2500 units	

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Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Char	acteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = 250 \mu\text{A}$	30			V
<u>ΔBV_{DSS}</u> ΔT _J	Breakdown Voltage Temperature Coefficient	I_D = 250 μ A, Referenced to 25°C		24		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 24 V, V _{GS} = 0 V			1	μΑ
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 20 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = -20 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 2)				-	
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1	1.6	3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	I_D = 250 μ A, Referenced to 25°C		-4		mV/°C
R _{DS(on)}	Static Drain-Source On-Resistance	$V_{GS} = 10 \text{ V}, I_D = 9.3 \text{ A}$ $V_{GS} = 10 \text{ V}, I_D = 9.3 \text{ A}$ $T_J @ 125^{\circ}\text{C}$ $V_{GS} = 4.5 \text{ V}, I_D = 8 \text{ A}$		0.015 0.022 0.019	0.018 0.030 0.025	Ω
I _{D(on)}	On-State Drain Current	V _{GS} = 10 V, V _{DS} = 5 V	20			Α
g FS	Forward Transconductance	V _{DS} = 5 V, I _D = 9.3 A		26		S
Dvnamic	Characteristics					
C _{iss}	Input Capacitance	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V},$		1160		pF
Coss	Output Capacitance	f = 1.0 MHz		250		pF
C _{rss}	Reverse Transfer Capacitance	1		100		pF
Switchin	g Characteristics (Note 2)					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 15 \text{ V}, I_D = 1 \text{ A},$		9	17	ns
t _r	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, R_{GEN} = 6 \Omega$		11	20	ns
t _{d(off)}	Turn-Off Delay Time	1		23	37	ns
t _f	Turn-Off Fall Time	1		8	16	ns
Q _g	Total Gate Charge	$V_{DS} = 15 \text{ V}, I_{D} = 9.3 \text{ A},$		12	17	nC
Q _{gs}	Gate-Source Charge	$V_{GS} = 5 V$		3.2		nC
Q _{gd}	Gate-Drain Charge	1		3.7		nC
Drain-So	ource Diode Characteristics and	d Maximum Ratings	•	•	•	
ls	Maximum Continuous Drain-Source Dic				2.1	Α
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 2.1 A (Note 2)		0.75	1.2	V

Notes:

1. $R_{\theta,JA}$ is the sum of the junction-to-case and case-to-ambient resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta,JC}$ is guaranteed by design while $R_{\theta,JA}$ is determined by the user's board design.





a) 50° C/W when mounted on a 1 in² pad of 2 oz. copper.

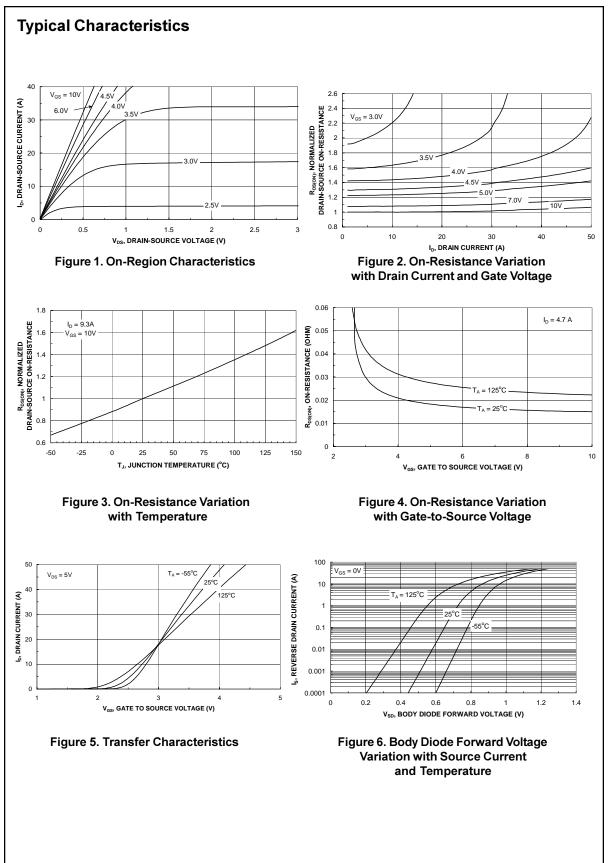
b) 105° C/W when mounted on a 0.04 in² pad of 2 oz. copper.

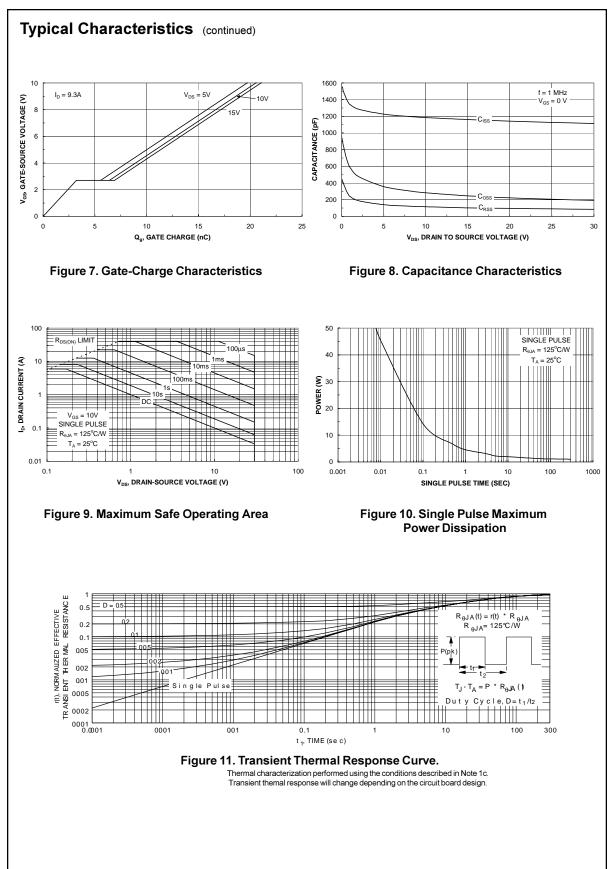
c) 125° C/W when mounted on a mounted on a minimum pad.



Scale 1 : 1 on letter size paper

2. Pulse Test: Pulse Width $\leq 300~\mu\text{s},~\text{Duty Cycle} \leq 2.0\%$





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Datasheet Identification	Product Status	Definition		
Advance Information Formative or In Design		This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.		
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