

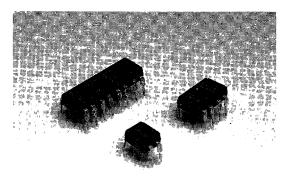
#### ■ FEATURES

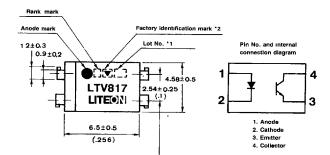
- 1. Current transfer ratio CTR: MIN. 50% at I<sub>F</sub> = 5mA,  $V_{CE}$  = 5V
- 2. High input-output isolation voltage (V<sub>ISO</sub>: 5,000 Vrms)
- Compact dual-in-line package LTV817: 1-channel type, LTV827: 2-channel type LTV847: 4-channel type
- 4. UL. approved (No. E 113898(s))

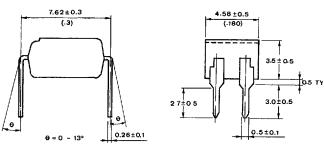
#### APPLICATIONS

- 1. Computer terminals
- 2. System appliances, measuring instruments
- 3. Registers, copiers, automatic vending machines
- 4. Electric home appliances such as fan heaters, etc.
- 5. Medical instruments, physical and chemical equipments.
- Signal transmission between circuits of different potentials and impedances

### OUTLINE DIMENSIONS (UNIT: mm)



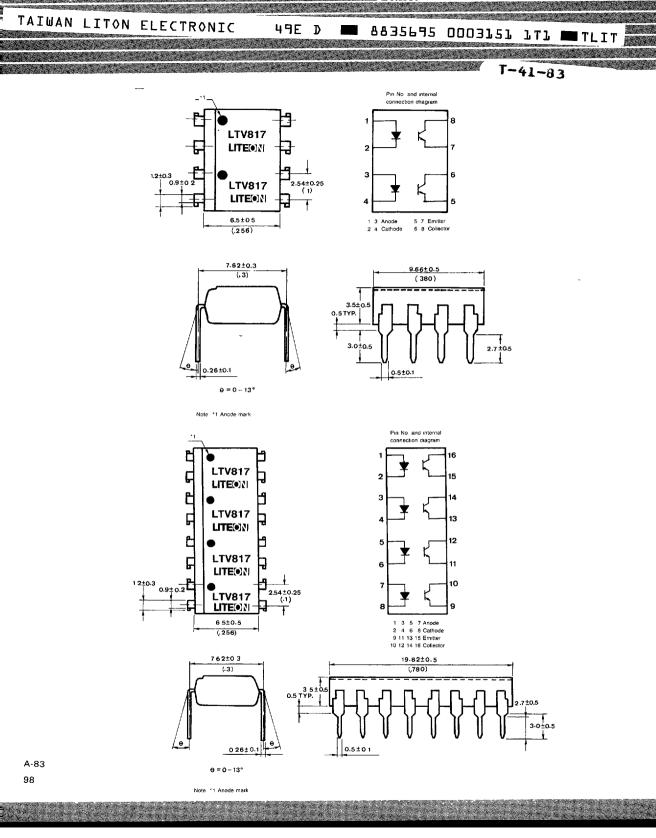




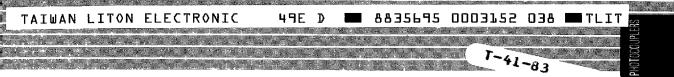
\*1 2-digit number marked according to DIN standard

"2 Two versions available, one with factory identification mark and the other without

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## RATINGS AND CHARACTERISTICS

Absolute maximum ratings

Parameter Symbol Rating Unit Forward current ١F 50 mΑ \*1 Peak forward current 1 А <sup>I</sup>FM Input Reverse voltage ٧R 6 V Р Power dissipation 70 mW 35 ٧ VCEO Collector-emitter voltage 6 V Emitter-collector voltage VECO Output Collector current IC 50 mΑ 150 PC mW Collector power dissipation Total power dissipation Ptot 200 mW Operating temperature °C Topr  $-30 \sim +100$ Storage temperature °C  $-55 \sim +125$ Tstg \* 2 Isolation voltage Viso 5 kVrms \*3 Soldering temperature °C Tsol 260

- \*1 Pulse width  $\leq 100\mu$ s, Duty ratio<sup>-</sup> 0.001
- \*2 AC for 1 minute,  $40 \sim 60\% R.H.$
- \*3 For 10 seconds

(Ta=25°C)

# TAIWAN LITON ELECTRONIC 49E D ■ 8835695 0003353 T74 ■ TLIT T-41-83

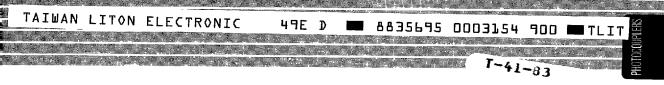
#### • Electro-optical characteristics

(Ta=25°C)

	Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input	Forward voltage	VF		1.2	1.4	V	IF=20mA
	Peak forward voltage	VFM	_		3.0	v	I <sub>FM</sub> =0.5A
	Reverse current	IR	_		10	μΑ	$V_{R} = 4V$
	Terminal capacitance	Ct	_	30	250	pF	V=0, f=1kHz
Output	Collector dark current	ICEO		_	100	nA	V <sub>CE</sub> =20V, I <sub>F</sub> =0, R <sub>BE</sub> =0
	Collector-emitter breakdown voltage	BVCEO	35			v	I <sub>C</sub> =0.1mA, I <sub>F</sub> =0
	Emitter-collector breakdown voltage	BVECO	6		_	v	$I_{E} = 10\mu A, I_{F} = 0$
Transfer characteristics	* Collector current	lc	2.5	_	30	mA	$I_F = 5 \text{mA}, V_{CE} = 5 \text{V}$
	Collector-emitter saturation voltage	V <sub>CE</sub> (sat)		0.1	02	v	I <sub>F</sub> =20mA, I <sub>C</sub> =1mA
	Isolation resistance	RISO	5×1010	1011	_	Ω	500V DC, 40~60% R.H.
	Floating capacitance	Cf		0.6	1.0	pF	V=0, f=1 MHz
	Cut-off frequency	fc	_	80		kHz	$V_{CE} = 5V, I_C = 2mA$ $R_L = 100\Omega, -3dB$
	Response time (Rise)	tr		4	18	μS	$V_{CE} = 2V, I_{C} = 2mA,$
	Response time (Fall)	t <sub>f</sub>		3	18	μs	$R_L = 100\Omega$

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#### SUPPLEMENT

#### Isolation voltage shall be measured in the following method.

- (1) Anode and cathode on input side, collector and emitter on output side shall be shortened individually.
- (2) The isolation voltage tester with a zero-cross circuit shall be used.
- (3) The waveform of applied voltage shall be a sine wave.
  - (It is recommended that the isolation voltage shall be measured in insulation oil.)

# Rank table of collector current IC (for LTV 817 only)

Model No.	Rank mark	IC (mA)
LTV817A	A	4.0~8.0
LTV817B	В	6.5~13
LTV817C	С	10~20
LTV817D	D	15~30
LTV817	A, B, C, D or No mark	2.5~30

#### Inspection standard

Outgoing inspection standard for LITON products are shown below.

(1) A single sampling plan, normal inspection level II based on MIL-STD-105D is applied. The AQL according to the inspection items are shown below.

Defect	Inspection item	AQL (%)	Judgement criterion	
Major defect	<ul> <li>Electrical characteristics</li> <li>Unreadable marking</li> <li>Open, short</li> </ul>	0 25	Depend on the specification	
Minor defect	<ul><li> Appearance</li><li> Dimension</li></ul>	0.4	specification	

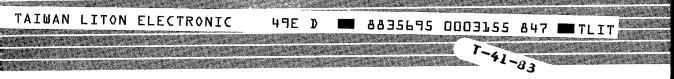


Fig. 1 Forward Current vs. Ambient Temperature

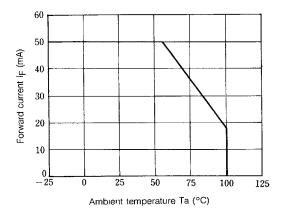


Fig. 3 Peak Forward Current vs. Duty Ratio

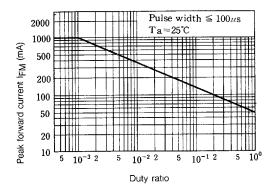


Fig. 5. Forward Current vs. Forward Voltage

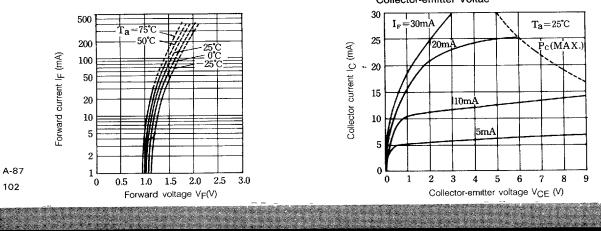


Fig. 2 Collector Power Dissipation vs. Ambient Temperature

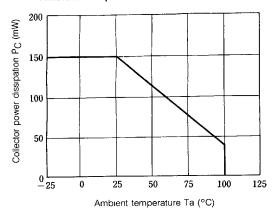


Fig. 4 Current Transfer Ratio vs. Forward Current

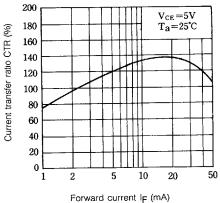


Fig. 6 Collector Current vs. Collector-emitter Voltae

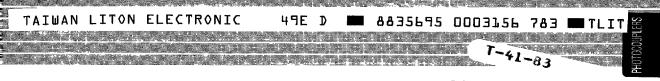
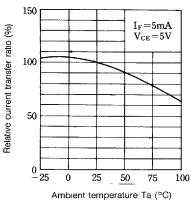
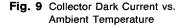


Fig. 7 Relative Current Transfer Ratio vs. Ambient Temperature





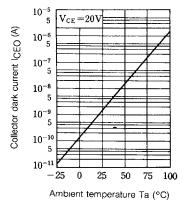


Fig. 11 Frequency Response

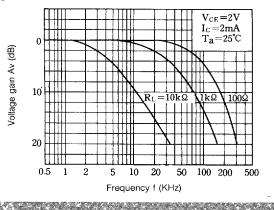


Fig. 8 Collector-emitter Saturation Voltage vs. Ambient Temperature

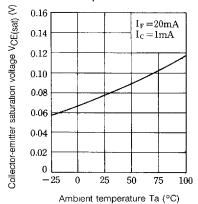


Fig. 10 Response Time vs. Load Resistance

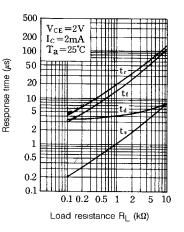
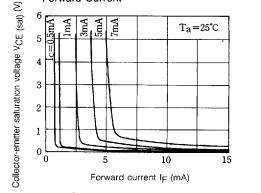
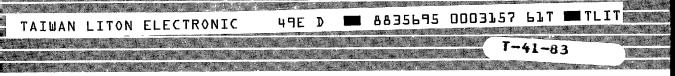


Fig. 12 Collector-emitter Saturation Voltage vs. Forward Current



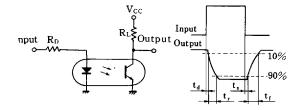
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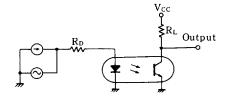
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Test Circuit for Response Time

Test Circuit for Frequency Response





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