TOSHIBA Photocoupler GaAs Ired & Photo-Transistor

TLP620, TLP620-2, TLP620-4

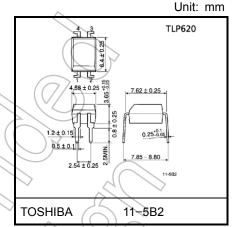
Programmable Controllers
AC / DC-Input Module
Telecommunication

The TOSHIBA TLP620, -2 and -4 consists of a photo–transistor optically coupled to two gallium arsenide infrared emitting diode connected in inverse parallel.

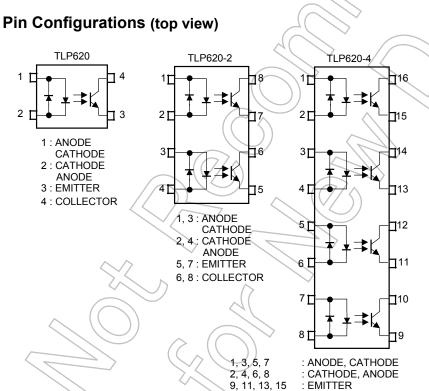
The TLP620–2 offers two isolated channels in an eight lead plastic DIP, while the TLP620–4 provides four isolated channels in a sixteen plastic DIP

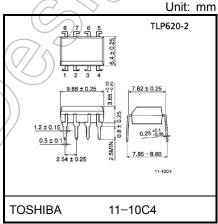
• Collector-emitter voltage: 55V (min.)

Current transfer ratio: 50% (min.)
 Rank GB: 100% (min.)

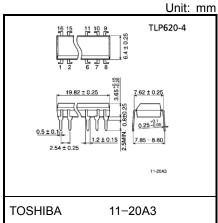


Weight: 0.26 g (typ.)





Weight: 0.54 g (typ.)



Weight: 1.1 g (typ.)

Start of commercial production 1984/01

10, 12, 14, 16 : COLLECTOR

• Isolation voltage: 5000V_{rms} (min.)

Safety Standards

• UL recognized : UL1577, File No. E67349

• cUL recognized : CSA Component Acceptance Service No. 5A

File No.E67349

• Option (D4) type

VDE approved : EN60747-5-5

Maximum operating insulation voltage: $890V_{PK}$ Highest permissible over voltage: $8000V_{PK}$

(Note) When an EN 60747-5-5 approved type is needed, please designate the "Option(D4)".

7.62 mm pitch	10.16 mm pitch
standard type	TLP×××F type
: 6.4 mm (min)	8.0 mm (min)

Creepage distance : 6.4 mm (min) 8.0 mm (min)

Clearance : 6.4 mm (min) 8.0 mm (min)

Insulation thickness : 0.4 mm (min) 0.4 mm (min)



Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Rati		
		Symbol	TLP620	TLP620-2 TLP620-4	Unit
	Forward current	IF (RMS)	60	60 50	
	Forward current derating	ΔI _F / °C	–0.7 (Ta ≥ 39°C)	–0.5 (Ta ≥ 25°C)	mA / °C
	Pulse forward current	IFP	±1 (100µs pu	ilse, 100pps)	Α
LED	Power dissipation (1 circuit)	PD	100	70	mW
	Power dissipation derating (1 circuit)	ΔP _D / °C	-1.2 (Ta ≥ 39°C)	–0.7 (Ta ≥ 25°C)	mW / °C
	Junction temperature	Tj	12	5 (//)	°C
	Collector-emitter voltage	V _{CEO}	55		V
	Emitter-collector voltage	V _{ECO}	(1)		V
_	Collector current	Ic	50		mA
Detector	Collector power dissipation (1 circuit)	PC	150	100	mW
	Collector power dissipation derating (1 circuit) (Ta ≥ 25°C)	ΔP _C / °C	-1.5		mW / °C
	Junction temperature	Tj	12	5	°C
Sto	rage temperature range	T _{stg}	-55~	125	°C
Оре	erating temperature range	T _{opr}	-55~100		°C
Lea	d soldering temperature	T _{sold}	260 (10s)		°C
	al package power dissipation ircuit)	PT &	250 150		mW
Total package power dissipation derating (Ta ≥ 25°C, 1 circuit) ΔPT / °C -2.5 -1.5		-1.5	mW / °C		
Isola	ation voltage (Note1)	BVs	5000 (AC, 1 mi	n., RH ≤ 60%)	V _{rms}

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: Device considered a two terminal device: LED side pins shorted together and detector side pins shorted together.



Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	Vcc	_	5	24	V
Forward current	IF (RMS)	_	16	25	mA
Collector current	IC	_	1	10	mA
Operating temperature	Topr	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

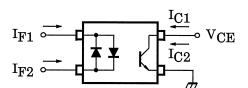
Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	I _F = ±10mA	1.0	1.15	1.3	V
LED	Forward current	lF	V _F = ±0.7V	~_(2.5	20	μΑ
	Capacitance	Ст	V = 0V, f = 1MHz	-//	60	_	pF
	Collector–emitter breakdown voltage	V (BR) CEO	IC ≠ 0.5mA	55)			٧
ctor	Emitter–collector breakdown voltage	V (BR) ECO	IE = 0.1mA	7	_	_	V
Detector	Collector dark current	loca	VCE = 24V	_	10	100	nA
	Collector dark current	ICEO	V _{CE} = 24V, Ta = 85°C	_	2	50	μΑ
	Capacitance (collector to emitter)	CCE	V _{CE} = 0V, f = 1MHz	_	10	_	pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Ic / IF	I _F = ±5mA, V _{CE} = 5V	50	_	600	%
Current transfer ratio	IC / IF	Rank GB	100	_	600	%
Saturated CTR	Io (IE V)	IF = ±1mA, V _{CE} = 0.4V Rank GB	1	60	1	%
Saturated CTR ICT	IC / IF (sat)		30	1	1	/0
		I _C = 2.4mA, I _F = ±8mA	1	1	0.4	
Collector–emitter saturation voltage	VCE (sat)	I _C = 0.2 mA, I _F = ±1 mA	-	0.2	_	V
		Rank GB	_	_	0.4	
Off-state collector current	Ic (off)	$V_F = \pm 0.7V$, $V_{CE} = 24V$		1	10	μΑ
CTR symmetry (Note.)	lc (ratio)	$I_{C} (I_{F} = -5mA) / I_{C} (I_{F} = +5mA)$	0.33	_	3	_

Note : $I_{C(ratio)} = \frac{I_{C2}(I_F = I_{F2}, V_{CE} = 5V)}{I_{C1}(I_F = I_{F1}, V_{CF} = 5V)}$





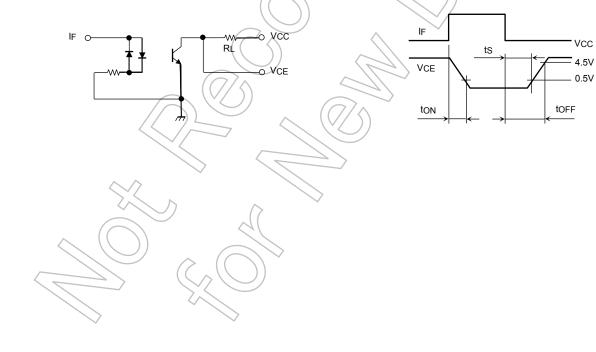
Isolation Characteristics (Ta = 25°C)

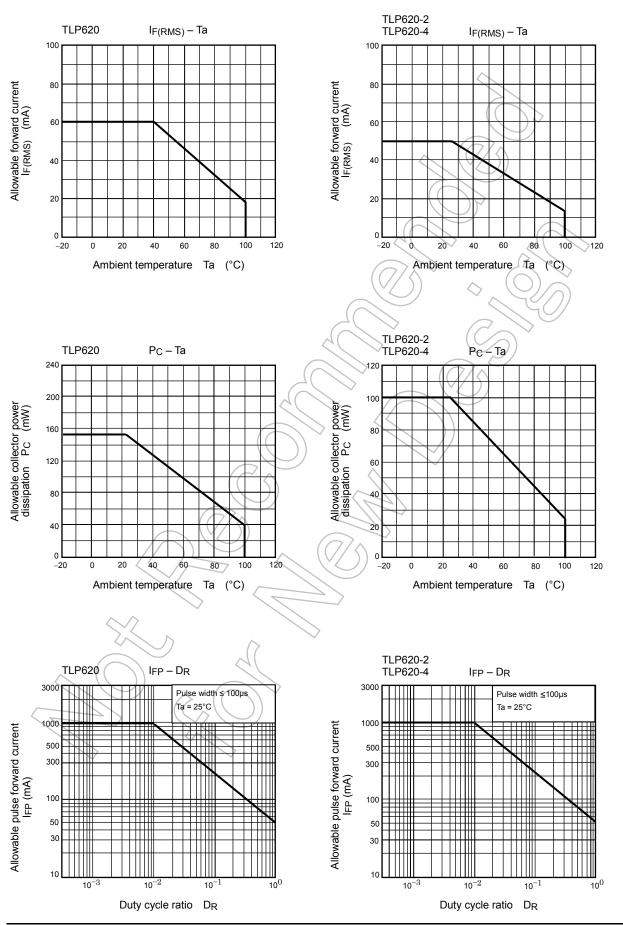
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	Cs	V _S = 0 V, f = 1MHz	_	0.8	-	pF
Isolation resistance	Rs	V _S = 500V, R.H. ≤ 60%	1×10 ¹²	10 ¹⁴	_	Ω
Isolation voltage	BVS	AC, 1 minute	5000	_	_	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
		AC, 1 second, in oil	(-)	10000	_	V _{rms}
		DC, 1 minute, in oil	7	10000	_	V _{dc}

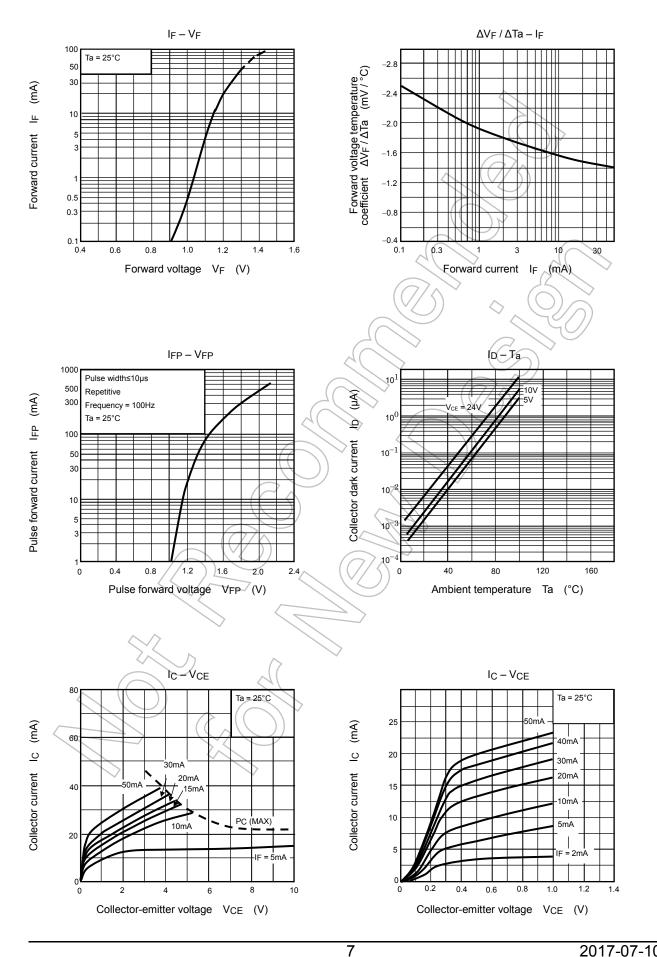
Switching Characteristics (Ta = 25°C)

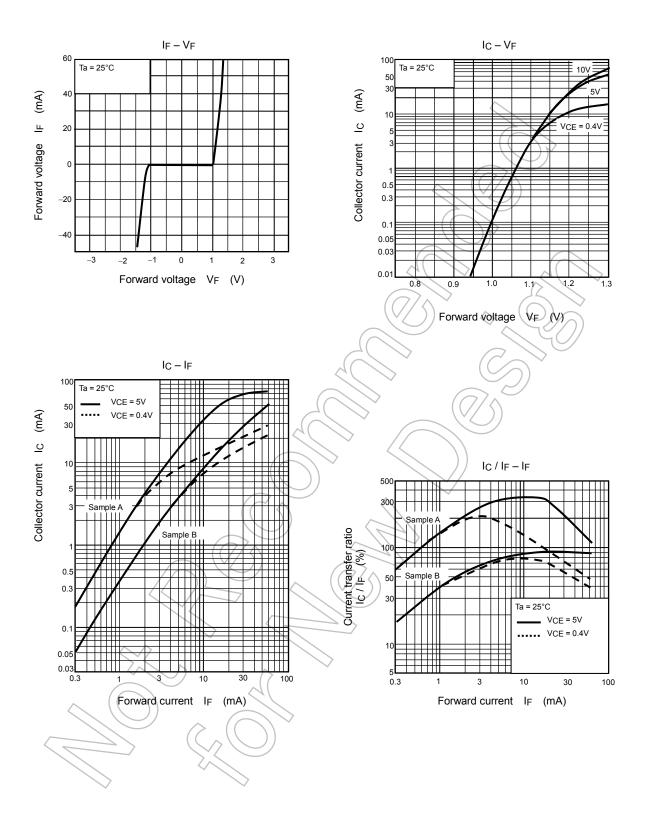
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise time	t _r	40	_	2	7	
Fall time	t _f	V _{CC} = 10V I _C = 2mA	- /	3	_	
Turn-on time	ton	$R_L = 100\Omega$	-(()3	_	μs
Turn-off time	t _{off}		(-	(3)	/ —	
Turn-on time	toN		7 (> 2	_	
Storage time	ts	$R_L = 1.9k\Omega$ (Fig.1) $V_{CC} = 5V$, $I_F = \pm 16mA$	/)	15	-	μs
Turn-off time	toff			25	_	

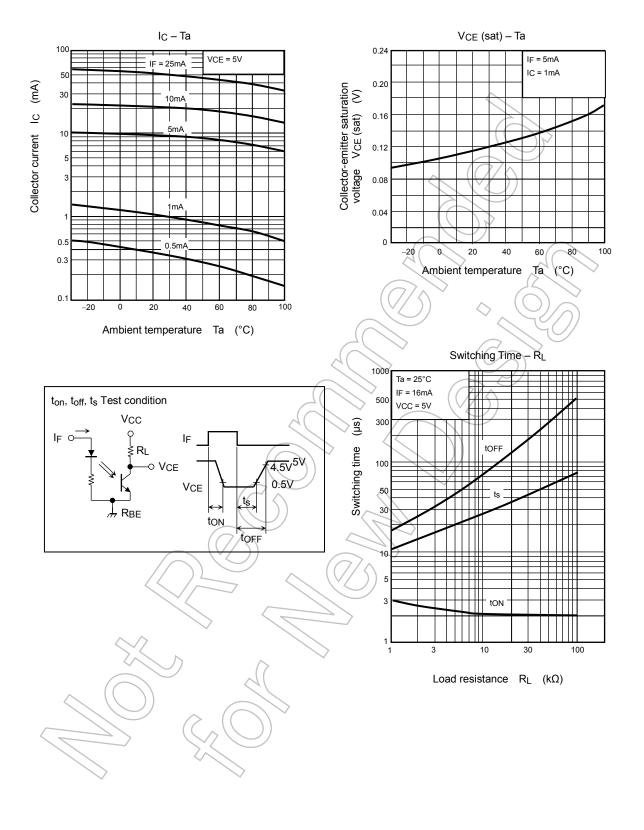
Fig. 1 Switching time test circuit











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