MN74HC266/MN74HC266S

Quad 2-Input Exclusive NOR Gates

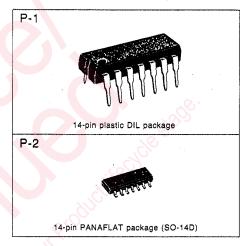
Outline

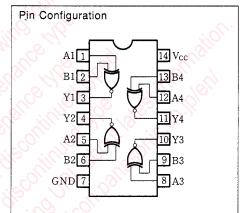
The MN74HC266/MN74HC266S is constituted by 2-input exclusive NOR gates having four built-in circuis in one chip. Owing to the silicon gate CMOS process, these NOR gates have realized low power consumption and high noise immunity equivalent to those of a standard CMOS and the operation speed as high as of an LS TTL. The respective output can directly drive ten LS TTL inputs.

To protect the input and output against electrostatic breakdown, a resistor and a diode are used for the V_{CC} and the GND. The pin configuration and the function are the same as those of the standard 54LS/74LS logic family.

■ Logic Diagram (1 Gate)







■ Absolute Maximum Ratings

Item			Symbol	Rating	Unit		
Supply voltage			V _{cc}	-0.5~+7.0	V		
Input output voltage			V _I , V _O	$-0.5 \sim V_{CC} + 0.5$	V		
Input protective diode current			I_{IK}	±20			
Output parasitic diode current			Іок	±20	mA		
Output current			Io	±25	mA		
Supply current			I _{CC} , I _{GND}	±50	mA		
Storage temperature			T_{stg}	−65~+150	°C		
Power dissipation	MN74HC266	Ta=-40~+60°C	D	400	mW		
		$Ta = +60 \sim +85^{\circ}C$	P_D	Decrease to 200mW at the rate of 8mW/°C			
	MNIZALICOGGE	Ta=-40~+60°C	D	275	mW		
	MN74HC266S	$Ta = +60 \sim +85^{\circ}C$	P_{D}	Decrease to 200mW at the rate of 3.8mW/°C	11177		

■ Recommended Operating Conditions

Item	Symbol	V _{CC} (V)	Rating	Unit	
Operating power supply voltage	V_{cc}		1.4~6.0	V	
Input output voltage	V _I , V _O		0∼V _{cc}	V	
Operating temperature	T _A		-40~+85	°C	
		2.0	0~1000	ns	
Input rise, fall time	t _r , t _f	4.5	0~500	ns	
		6.0	0~400	ns	

■ DC Characteristics (GND=0V)

Item	Symbol	V _{cc} (V)	Test Condition			Temperature					
			V _I	I _O		Ta=25°C			Ta=-40~+85°C		Unit
				10	Unit	min.	typ.	max.	min.	max.	
		2.0				1.5		,	1.5		
Input voltage high level	V _{IH}	4.5				3.15		5,	3.15		V
		6.0		1		4.2		63-	4.2		
		2.0						0.3		0.3	
Input voltage low level	V_{IL}	4.5					, c'	0.9		0.9	V
		6.0					777	1.2		1.2	
		2.0		-20.0	μΑ	1.9	2.0		1.9		
		4.5	V_{IH}	-20.0	μΑ	4.4	4.5		4.4		
Output voltage high level	Voh	6.0	or	-20.0	μΑ 🐧	5.9	6.0		5.9		V
		4.5	V_{iL}	-4.0	mA	3.92	5		3.84	4	
		6.0		-5.2	mA	5.48			5.34	0/4	
		2.0		20.0	μΑ		0.0	0.1		0.1	
		4.5	V_{IH}	20.0	μ A		0.0	0.1		0.1	
Output voltage low level	Vol	6.0	or	20.0	μ A		0.0	0.1		0.1	V
		4.5	VIL	4.0	mA		0	0.26		0.33	
		6.0		5.2	mA		0,	0.26	F .0	0.33	
Input leakage current	$I_{\rm f}$	6.0	$V_I = V_{CC}$	or GNL)8)	4	700	±0.1		±1.0	μA
Static supply current	I_{cc}	6.0	$V_I = V_{CC}$	or GNE	$I_0 = 0$	2, '(5 6	2.0	11,00	20.0	μΑ

■ AC Characteristics (GND=0V, Input transition time≦6ns, C_L=50pF)

	Symbol	V _{cc} (V)	Test Condition	9					
Item				Ta=25°C			Ta=-40~+85°C		Unit
				min.	typ.	max.	min.	max.	
		2.0	110	, %	25	75		95	
Output rise time	t _{TLH}	4.5	(0)	1,50	8	15		19	ns
		6.0		11.	7	13		16	
		2.0	113 114		20	75		95	
Output fall time	t _{THL}	4.5	S "O';),		7	15		19	ns
<u> </u>		6.0	100 Mills		6	13	l	16	
		2.0	610		25	75		95	
Propagation time (L→H)	t _{PLH}	4.5			8	15		19	ns
		6.0			7	13		16	
		2.0			25	75		95	
Propagation time $(H\rightarrow L)$	t _{PHL}	4.5			8	15		19	ns
		6.0			7	13		16	

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