Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

2SK3131

Chopper Regulator DC-DC Converter and Motor Drive Applications

• Fast reverse recovery time $: t_{rr} = 105 \text{ ns (typ.)}$

• Built-in high-speed free-wheeling diode

• Low drain-source ON resistance : RDS (ON) = 0.085Ω (typ.)

• High forward transfer admittance : $|Y_{fs}| = 35 S \text{ (typ.)}$

• Low leakage current : $IDSS = 100 \mu A (max) (VDS = 500 V)$

• Enhancement-mode $V_{th} = 2.4 \sim 3.4 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$

Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	500	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	500	V	
Gate-source voltage		V _{GSS}	±30	V	
DC Drain current	DC (Note 1)	I _D	50	Α	
	Pulse (Note 1)	I_{DP}	200	Α	
Drain power dissipation	n (Tc = 25°C)	P_{D}	250	W	
Single pulse avalanche energy (Note 2)		E _{AS}	525	mJ	
Avalanche current		I _{AR}	50	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	25	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	

Weight: 9.75 g (typ.)

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	0.5	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	35.7	°C/W

Note 1: Please use devices on condition that the channel temperature is below 150°C.

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Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 357 μ H, R_G = 25 Ω , I_{AR} = 50 A

Note 3: Repetitive rating; Pulse width limited by maximum channel temperature.

This transistor is an electrostatic sensitive device.

Please handle with caution.

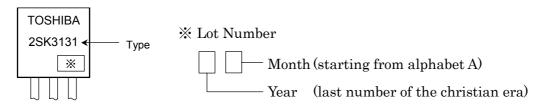
Electrical Characteristics (Ta = 25°C)

Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I_{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	_	_	±10	μA
Gate-source bre	eakdown voltage	V (BR) GSS	I _G = ±100 μA, V _{DS} = 0 V	±30	_	_	V
Drain cut-off cur	rent	I _{DSS}	V _{DS} = 500 V, V _{GS} = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	500	_	_	٧
Gate threshold v	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.4	_	3.4	V
Drain-source Ol	N resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 25 A	_	0.085	0.11	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 25 A	15	35	_	S
Input capacitanc	е	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		11000	_	pF
Reverse transfer	capacitance	C _{rss}			2100	_	
Output capacitance		C _{oss}			4200	_	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{\underset{OV}{}} \stackrel{I_{D}=25A}{\underset{R_{L}=8\Omega}{}} $	_	105	_	
	Turn-on time	t _{on}		l	160	ı	ne
	Fall time	t _f		l	65	ı	ns
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\mathbf{W}} = 10 \mu s$		245		
Total gate charge (Gate-source plus gate-drain)		Qg			280	ı	
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 50 \text{ A}$		150	_	nC
Gate-drain ("miller") charge		Q_{gd}			130	_	

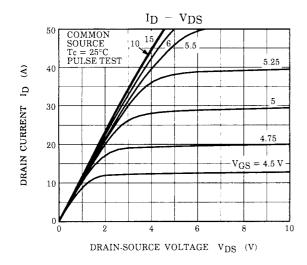
Source-Drain Ratings and Characteristics (Ta = 25°C)

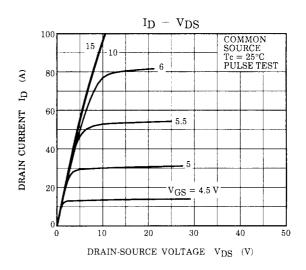
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	50	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	200	Α
Forward voltage (diode)	V _{DSF}	V _{DR} = 25 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 50 A, V _{GS} = 0 V dI _{DR} / dt = 100 A / μs	_	105	_	ns
Reverse recovery charge	Q _{rr}		_	380	_	nC

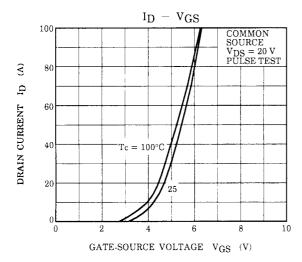
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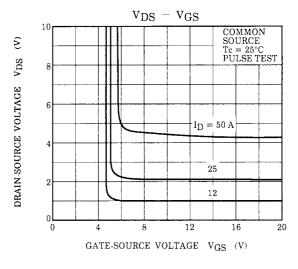


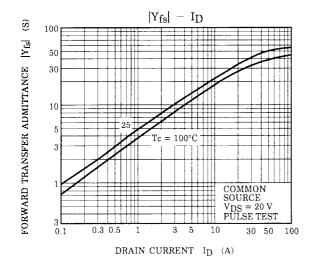
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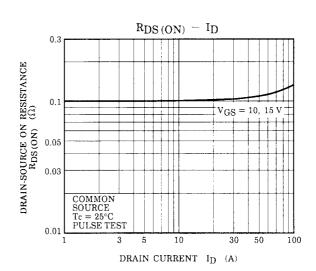




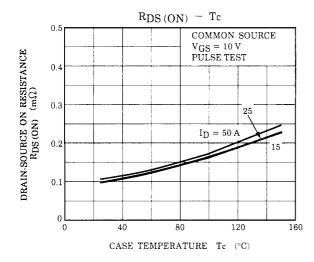


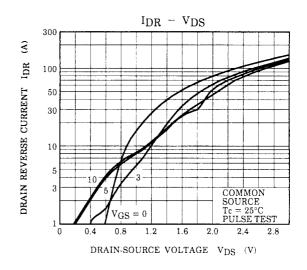


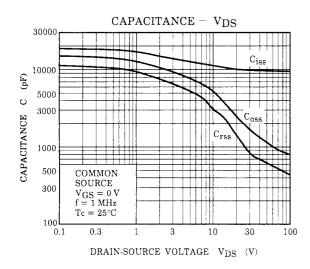


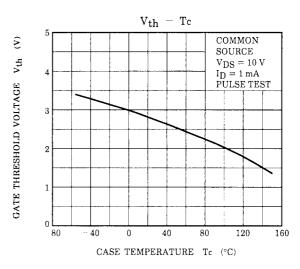


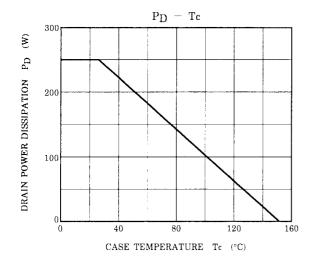
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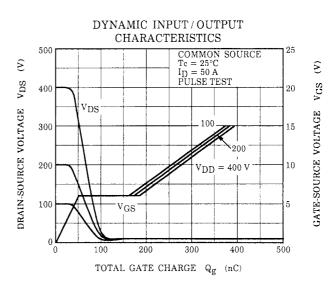




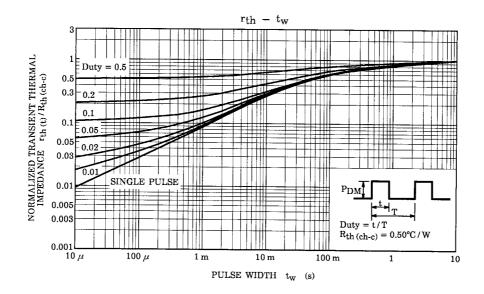


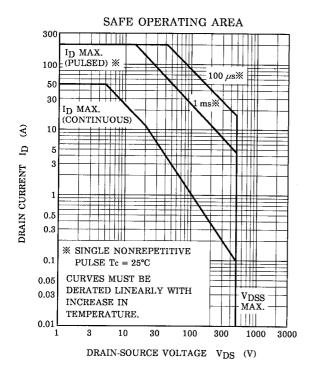


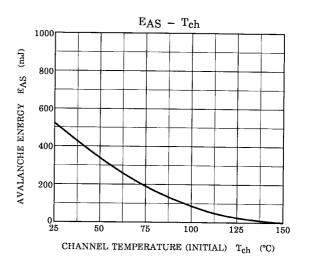


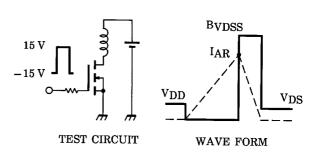


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$$R_G = 25 \Omega$$

 $V_{DD} = 90 \text{ V}, L = 357 \mu\text{H}$

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$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS - VDD} \right)$$

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