

TLP181

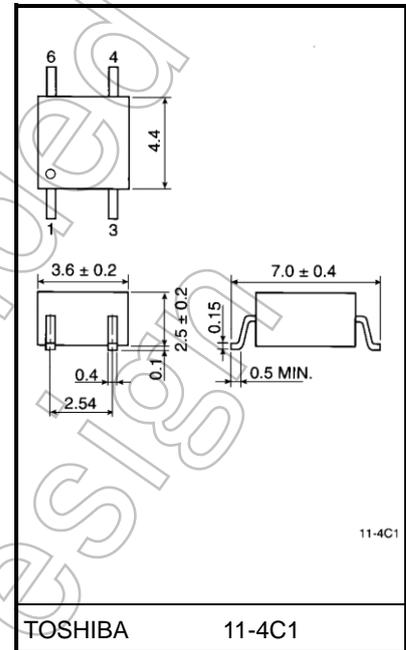
Office Machine
 Programmable Controllers
 AC Adapter
 I/O Interface Board

Unit: mm

The TOSHIBA mini flat coupler TLP181 is a small outline coupler, suitable for surface mount assembly. TLP181 consist of a photo transistor optically coupled to a gallium arsenide infrared emitting diode. Since TLP181 is smaller than DIP package, it's suitable for high-density surface mounting applications such as programmable controllers.

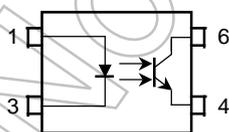
- Collector-emitter voltage: 80 V (min)
- Current transfer ratio: 50% (min)
 Rank GB: 100% (min)
- Isolation voltage: 3750 Vrms (min)
- Operation Temperature: -55 to 110 °C
- Safety Standards
 UL recognized: UL1577, File No. E67349
 cUL recognized: CSA Component Acceptance Service No. 5A
 File No.E67349
- Option (V4) type
 VDE approved : EN60747-5-5
 Maximum Operating Insuration Voltage: 565 Vpk
 Highest Permissible Overvoltage: 6000 Vpk

Note: When a EN60747-5-5 approved type is needed,
 Please designate "Option(V4)"



Weight: 0.09 g (Typ.)

Pin Configuration (top view)



- 1: Anode
- 3: Cathode
- 4: Emitter
- 6: Collector

Start of commercial production
 1993-05

Current Transfer Ratio

Classification (Note 1)	Current Transfer Ratio (%) (I _C /I _F)		Marking Of Classification
	I _F = 5mA, V _{CE} = 5V, T _a = 25°C		
	Min	Max	
Blank	50	600	Blank, Y [■] , YE, G, G [■] , GR, B, BL, GB
Rank Y	50	150	YE, Y [■]
Rank GR	100	300	GR, G, G [■]
Rank BL	200	600	BL, B
Rank GB	100	600	GB, GR, G, G [■] , BL, B
Rank YH	75	150	Y [■]
Rank GRL	100	200	G
Rank GRH	150	300	G [■]
Rank BLL	200	400	B

Note 1: EX, Rank GB: TLP181 (GB)

Note: Application, type name for certification test, please use standard product type name, i. e.
TLP181 (GB): TLP181

Not Recommended for New Design

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit
LED	Forward current	I _F	50	mA
	Forward current derating (Ta ≥ 89°C)	ΔI _F /°C	-1.4	mA/°C
	Pulse forward current (100μs pulse, 100pps)	I _{FP}	1	A
	Reverse voltage	V _R	5	V
	Diode power dissipation	P _D	100	mW
	Diode power dissipation derating (Ta ≥ 89°C)	ΔP _D /°C	-2.8	mW/°C
	Junction temperature	T _j	125	°C
Detector	Collector-emitter voltage	V _{CEO}	80	V
	Emitter-collector voltage	V _{ECO}	7	V
	Collector current	I _C	50	mA
	Collector power dissipation	P _C	150	mW
	Collector power dissipation derating (Ta ≥ 25°C)	ΔP _C /°C	-1.5	mW/°C
	Junction temperature	T _j	125	°C
Storage temperature range		T _{stg}	-55 to 125	°C
Operating temperature range		T _{opr}	-55 to 110	°C
Lead soldering temperature (10 s)		T _{sol}	260	°C
Total package power dissipation		P _T	200	mW
Total package power dissipation derating (Ta ≥ 25°C)		ΔP _T /°C	-2.0	mW/°C
Isolation voltage (AC, 60 s, R.H. ≤ 60%) (Note 1)		BV _S	3750	V _{rms}

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two-terminal device: Pin1, 3 shorted together and pins 4, 6 shorted together

Recommended Operating Conditions

Characteristic	Symbol	Min	Typ.	Max	Unit
Supply voltage	V _{CC}	—	5	48	V
Forward current	I _F	—	16	20	mA
Collector current	I _C	—	1	10	mA

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Typ.	Max	Unit
LED	Forward voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
	Reverse current	I _R	V _R = 5 V	—	—	10	μA
	Capacitance	C _T	V = 0 V, f = 1 MHz	—	30	—	pF
Detector	Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C = 0.5 mA	80	—	—	V
	Emitter-collector breakdown voltage	V _{(BR)ECO}	I _E = 0.1 mA	7	—	—	V
	Collector dark current	I _{CEO}	V _{CE} = 48 V, (Ambient light below 1000 lx) (Note 1)	—	0.01 (2)	0.1 (10)	μA
			V _{CE} = 48 V, Ta = 85°C, (Ambient light below 1000 lx) (Note 1)	—	2 (4)	50 (50)	μA
Capacitance (collector to emitter)	C _{CE}	V = 0 V, f = 1 MHz	—	10	—	pF	

Note 1: Please use standard electric lamp to light up the device's marking surface.

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Current transfer ratio	I _C /I _F	I _F = 5 mA, V _{CE} = 5 V Rank GB	50	—	600	%
			100	—	600	
Saturated CTR	I _C /I _{F(sat)}	I _F = 1 mA, V _{CE} = 0.4 V Rank GB	—	60	—	%
			30	—	—	
Collector-emitter saturation voltage	V _{CE(sat)}	I _C = 2.4 mA, I _F = 8 mA I _C = 0.2 mA, I _F = 1 mA Rank GB	—	—	0.4	V
			—	0.2	—	
			—	—	0.4	
Off-state collector current	I _{C(off)}	V _F = 0.7 V, V _{CE} = 48 V	—	1	10	μA

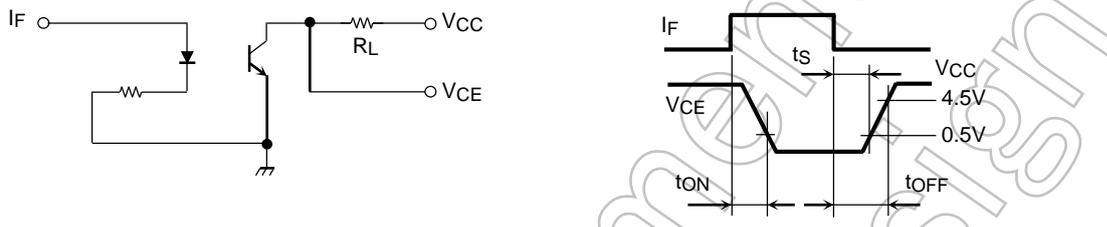
Isolation Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Capacitance (input to output)	C _S	V _S = 0 V, f = 1 MHz	—	0.8	—	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≤ 60%	1×10 ¹²	10 ¹⁴	—	Ω
Isolation voltage	BV _S	AC, 60 s	3750	—	—	V _{rms}
		AC, 1 s, in oil	—	10000	—	
		DC, 60 s, in oil	—	10000	—	V _{dc}

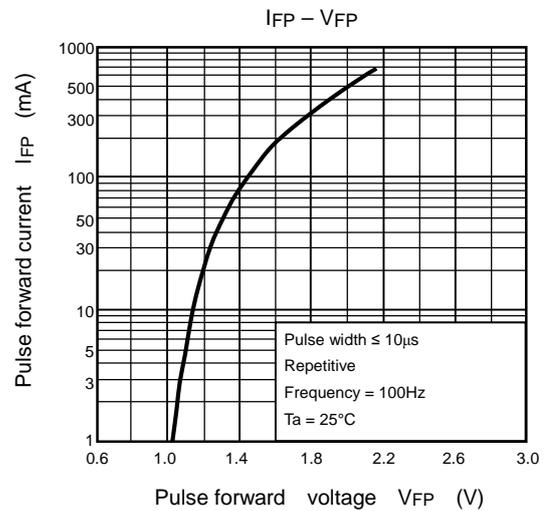
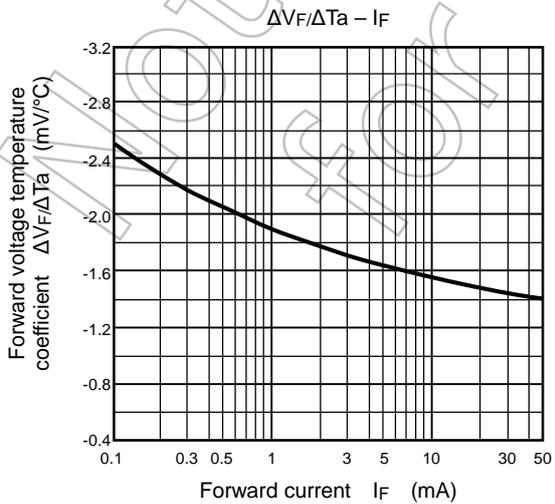
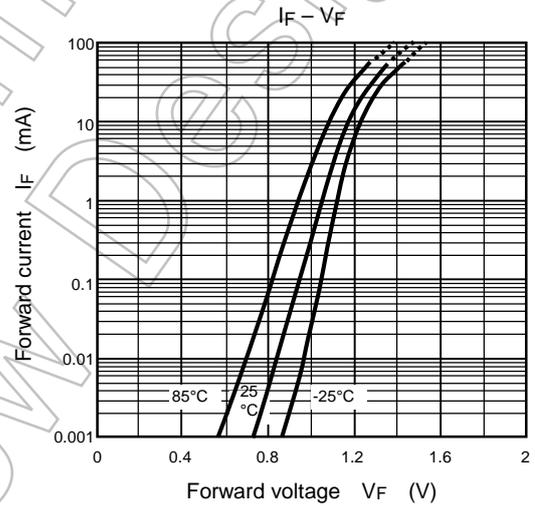
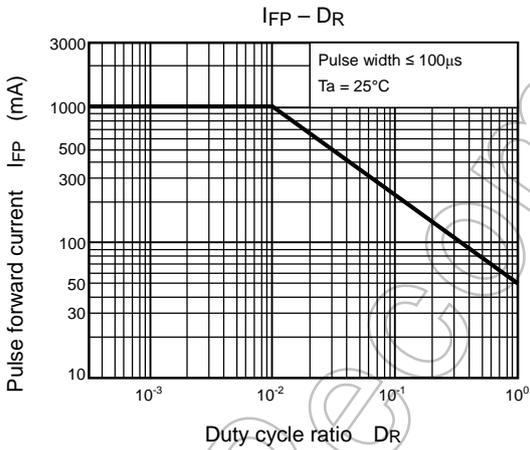
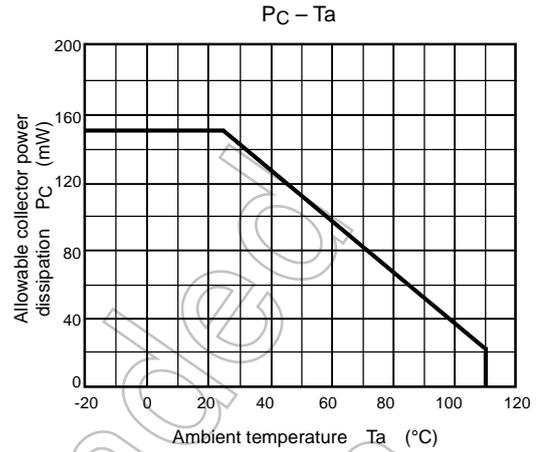
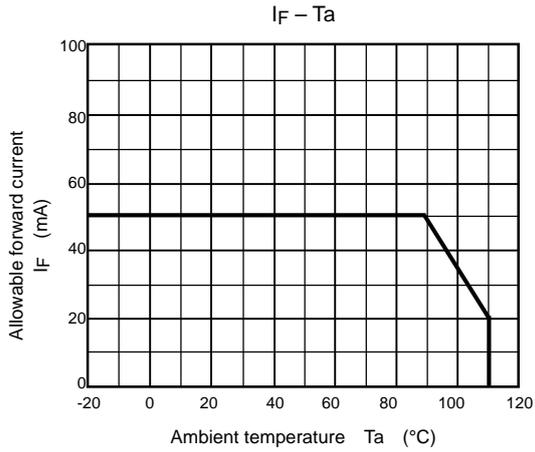
Switching Characteristics (Ta = 25°C)

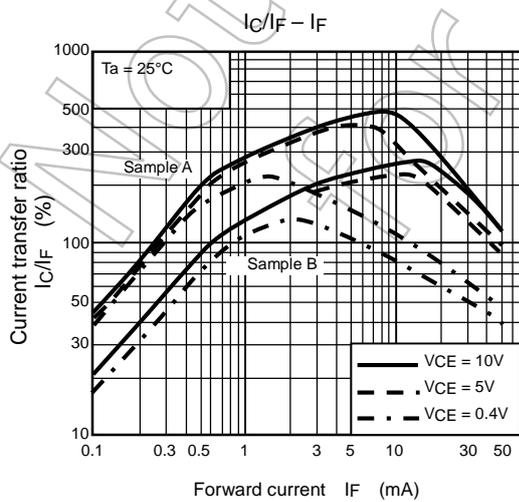
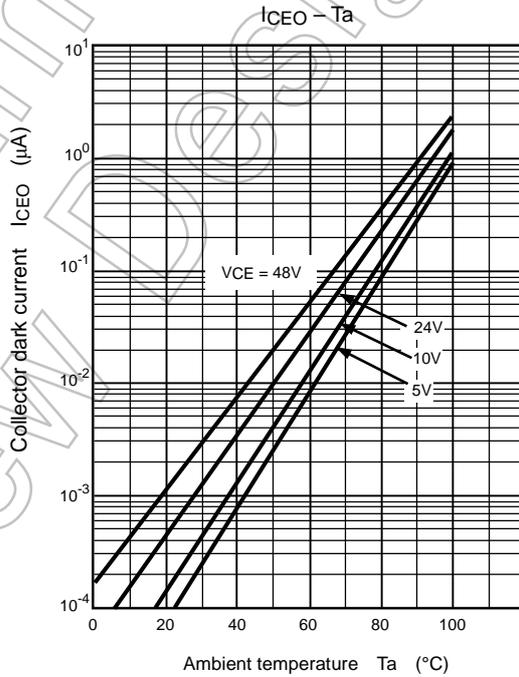
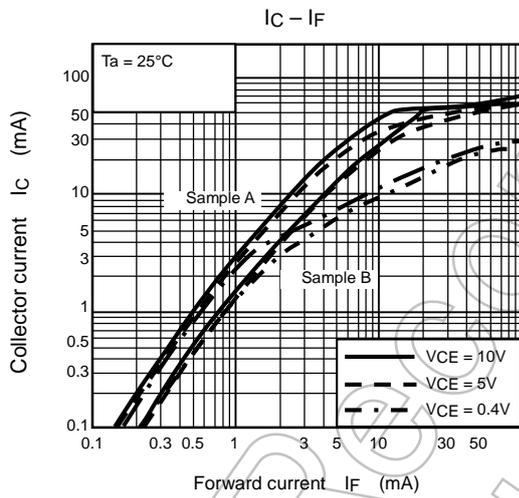
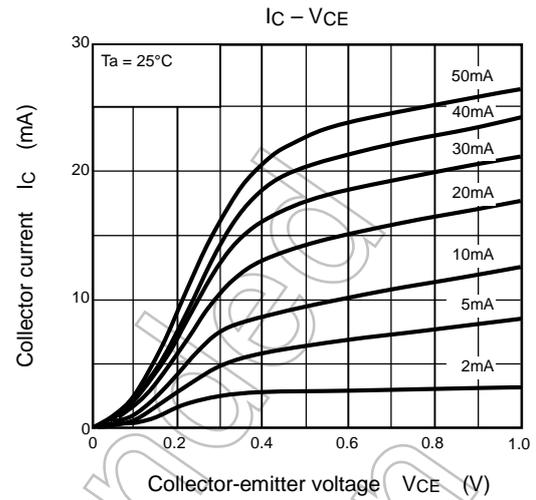
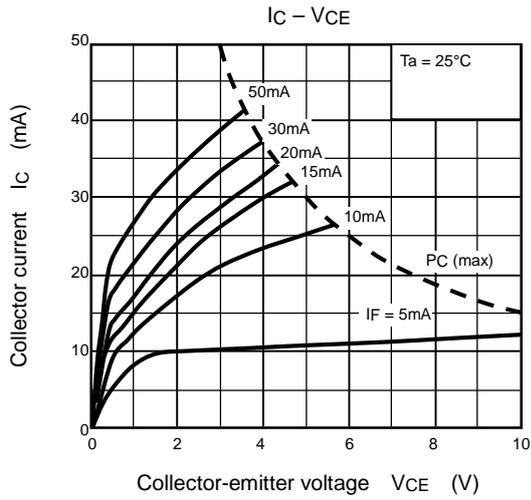
Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Rise time	t_r	$V_{CC} = 10\text{ V}$, $I_C = 2\text{ mA}$ $R_L = 100\Omega$	—	2	—	μs
Fall time	t_f		—	3	—	
Turn-on time	t_{on}		—	3	—	
Turn-off time	t_{off}		—	3	—	
Turn-on time	t_{ON}	$R_L = 1.9\text{ k}\Omega$ $V_{CC} = 5\text{ V}$, $I_F = 16\text{ mA}$ (Fig.1)	—	2	—	μs
Storage time	t_s		—	25	—	
Turn-off time	t_{OFF}		—	40	—	

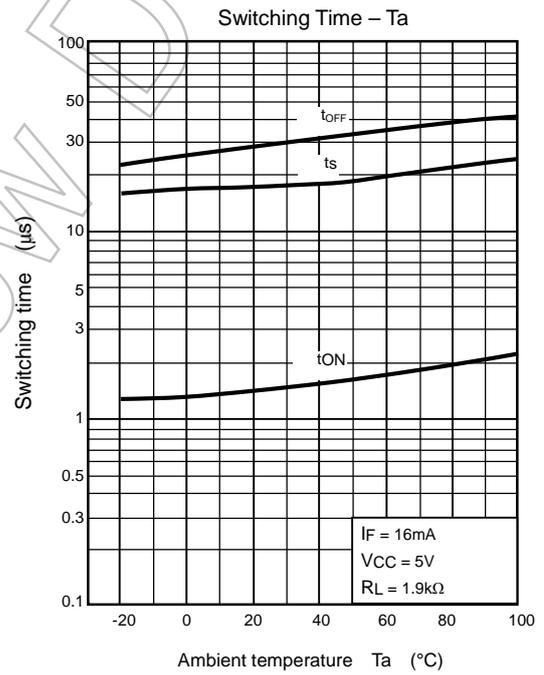
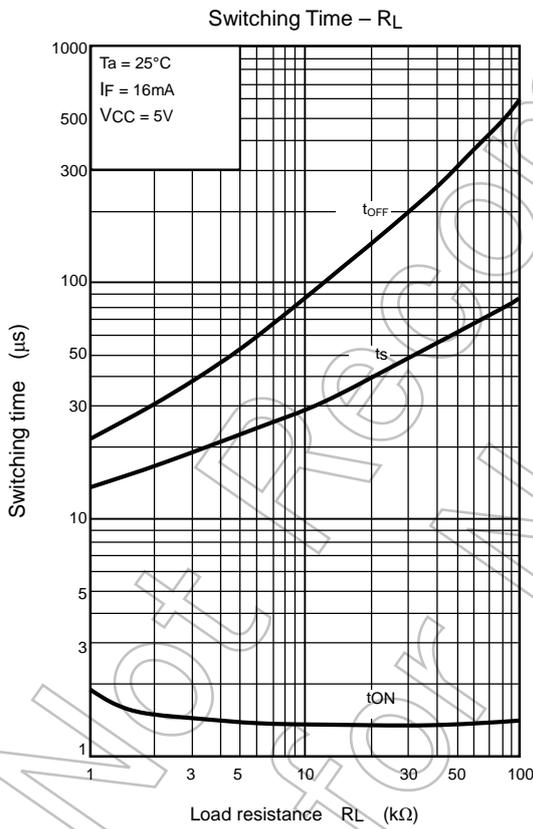
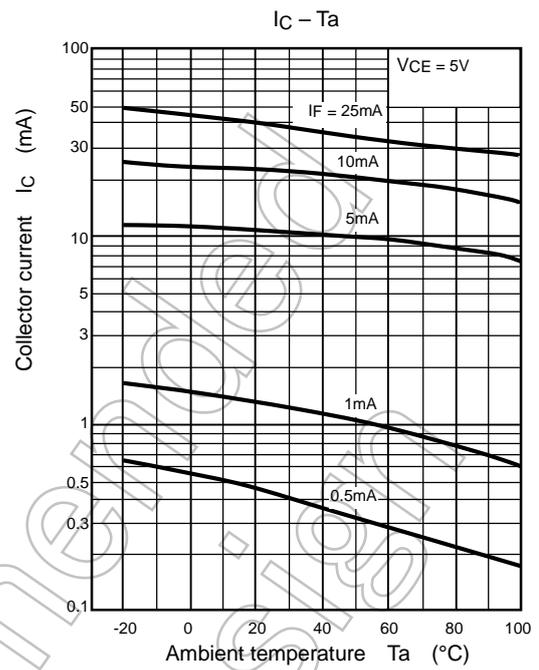
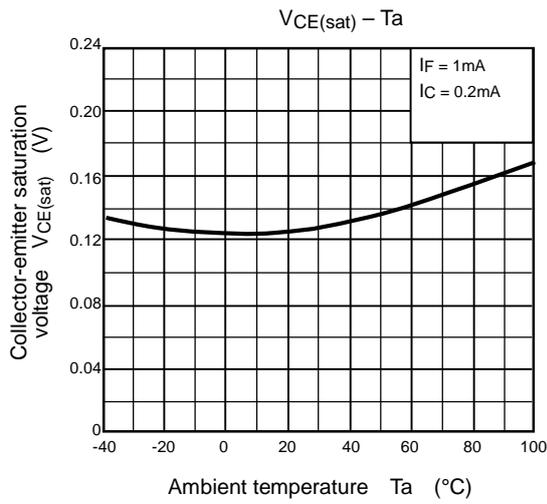
Fig. 1 Switching time test circuit



Not Recommended for New Design







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