Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSVI)

2SK3561

Switching Regulator Applications

Low drain-source ON resistance: R_{DS} (ON) = 0.75 Ω (typ.)

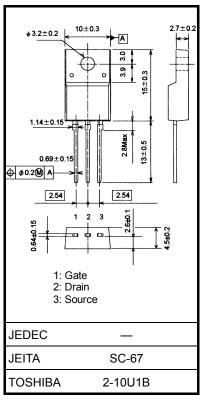
• High forward transfer admittance: |Y_{fs}| = 6.5 S (typ.)

• Low leakage current: $I_{DSS} = 100 \mu A (V_{DS} = 500 V)$

• Enhancement mode: V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	500	V	
Drain-gate voltage (F	$R_{GS} = 20 \text{ k}\Omega$	V_{DGR}	500	V	
Gate-source voltage		V_{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	8	А	
	Pulse (t = 1 ms) (Note 1)	I _{DP}	32		
Drain power dissipati	on (Tc = 25°C)	P _D	40	W	
Single pulse avalanche energy (Note 2)		E _{AS}	312	mJ	
Avalanche current		I _{AR}	8	Α	
Repetitive avalanche	energy (Note 3)	E _{AR}	4	mJ	
Channel temperature	;	T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

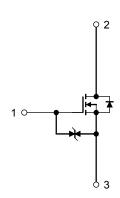
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	3.125	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}(\text{initial})$, L = 8.3 mH, $I_{AR} = 8 \text{ A}$, $R_G = 25 \Omega$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.



Electrical Characteristics (Ta = 25°C)

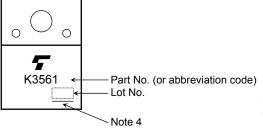
Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Gate-source brea	akdown voltage	V (BR) GSS	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30	_	_	V
Drain cut-off current		I _{DSS}	V _{DS} = 500 V, V _{GS} = 0 V	_	_	100	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	500	_	_	V
Gate threshold voltage		V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source ON resistance		R _{DS} (ON)	V _{GS} = 10 V, I _D = 4 A	_	0.75	0.85	Ω
Forward transfer admittance		Yfs	V _{DS} = 10 V, I _D = 4 A	3.0	6.5	_	S
Input capacitance		C _{iss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	1050	_	pF
Reverse transfer capacitance		C _{rss}		_	10	_	
Output capacitance		Coss		_	110	_	
Switching time	Rise time	t _r	$\begin{array}{c c} 10 \text{ V} & \text{ID} = 4 \text{ A} & \text{Vout} \\ \hline VGS & \\ 0 \text{ V} & \\ \hline SO \Omega & \\ \end{array}$ $\begin{array}{c c} R_L = \\ 50 \Omega \\ \end{array}$ $\begin{array}{c c} V_{DD} \approx 200 \text{ V} \end{array}$	_	26	_	
	Turn-on time	t _{on}		_	45	_	20
	Fall time	t _f		_	38	_	ns
	Turn-off time	t _{off}	Duty \leq 1%, $t_W = 10 \mu s$	_	130	_	
Total gate charge		Qg		_	28	_	
Gate-source charge		Q _{gs}	$V_{DD} \simeq 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 8 \text{ A}$	_	16	_	nC
Gate-drain charge		Q _{gd}			12	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I_{DR}	_	_	_	8	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	32	Α
Forward voltage (diode)	V _{DSF}	$I_{DR} = 8 \text{ A}, V_{GS} = 0 \text{ V}$			-1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = 8 \text{ A}, V_{GS} = 0 \text{ V},$	_	1200		ns
Reverse recovery charge	Qrr	dI _{DR} /dt = 100 A/μs	_	10	_	μС

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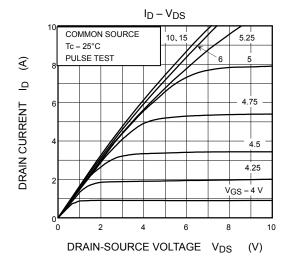
Marking

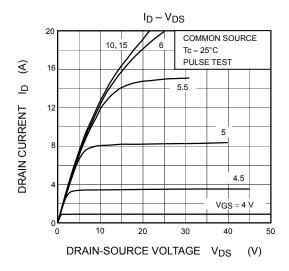


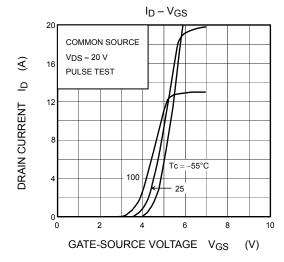
Note 4: A line under a Lot No. identifies the indication of product Labels.

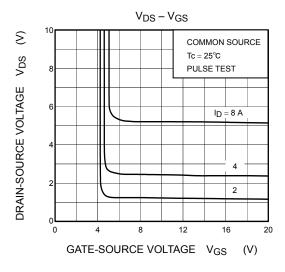
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

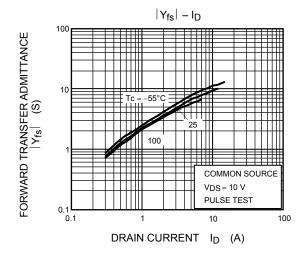
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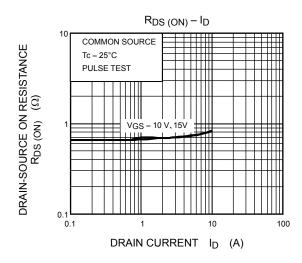




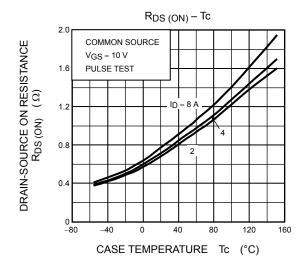


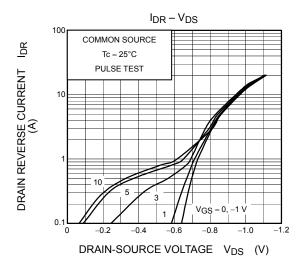


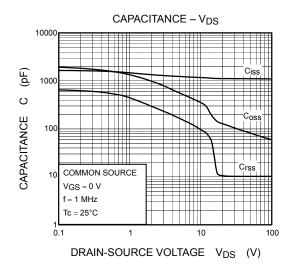


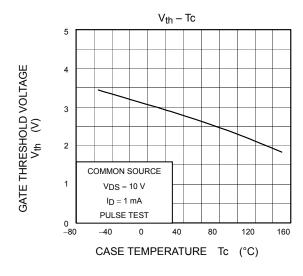


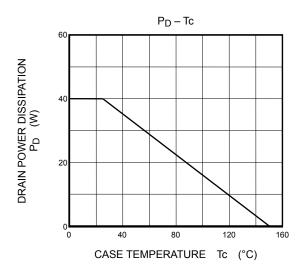
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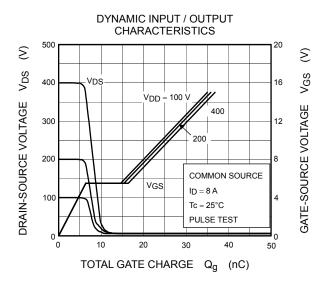




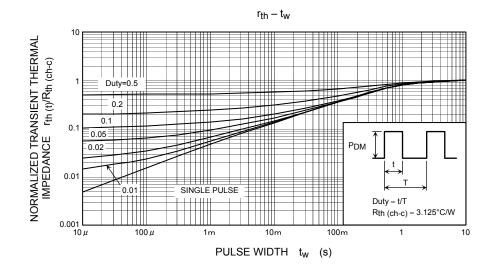


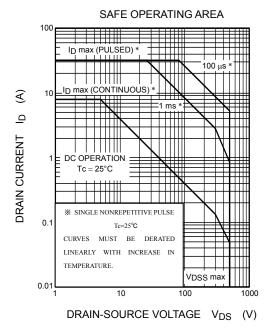


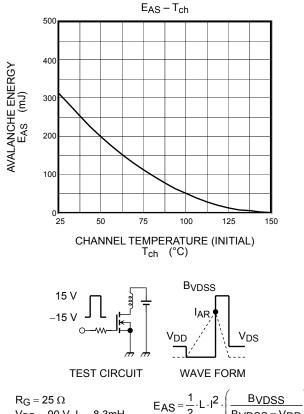




4







$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 90~V,~L = 8.3 mH \end{aligned}$$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

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