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April 1988 Revised June 2003

#### 74F86

### 2-Input Exclusive-OR Gate

#### **General Description**

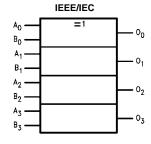
This device contains four independent gates, each of which performs the logic exclusive-OR function.

#### **Ordering Code:**

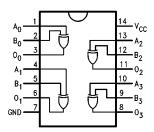
Order Number	Package Number	Package Description
74F86SC	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow
74F86SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
74F86PC	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

#### **Logic Symbol**



#### **Connection Diagram**



#### **Unit Loading/Fan Out**

Pin Names	Description	U.L. HIGH/LOW	Input I <sub>IH</sub> /I <sub>IL</sub> Output I <sub>OH</sub> /I <sub>OL</sub>		
A <sub>n</sub> , B <sub>n</sub>	Inputs	1.0/1.0	20 μA/-0.6 mA		
O <sub>n</sub>	Outputs	50/33.3	-1 mA/20 mA		

#### Absolute Maximum Ratings(Note 1)

Storage Temperature -65°C to +150°C

 $\begin{array}{lll} \mbox{Ambient Temperature under Bias} & -55^{\circ}\mbox{C to } +125^{\circ}\mbox{C} \\ \mbox{Junction Temperature under Bias} & -55^{\circ}\mbox{C to } +150^{\circ}\mbox{C} \\ \mbox{V}_{\mbox{CC}} \mbox{ Pin Potential to Ground Pin} & -0.5V \mbox{ to } +7.0V \\ \end{array}$ 

Input Voltage (Note 2) -0.5 V to +7.0 V Input Current (Note 2) -30 mA to +5.0 mA

Voltage Applied to Output in HIGH State (with V<sub>CC</sub> = 0V)

 $\begin{array}{ll} \mbox{Standard Output} & -0.5\mbox{V to V}_{\mbox{CC}} \\ \mbox{3-STATE Output} & -0.5\mbox{V to +5.5V} \end{array}$ 

Current Applied to Output

in LOW State (Max)  $\qquad \qquad \text{twice the rated I}_{OL} \, (\text{mA})$ 

## Recommended Operating Conditions

Free Air Ambient Temperature 0°C to +70°C Supply Voltage +4.5V to +5.5V

Note 1: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: Either voltage limit or current limit is sufficient to protect inputs.

#### **DC Electrical Characteristics**

Symbol	Parameter	Min	Тур	Max	Units	v <sub>cc</sub>	Conditions
V <sub>IH</sub>	Input HIGH Voltage	2.0			V		Recognized as a HIGH Signal
V <sub>IL</sub>	Input LOW Voltage			0.8	V		Recognized as a LOW Signal
V <sub>CD</sub>	Input Clamp Diode Voltage			-1.2	V	Min	$I_{IN} = -18 \text{ mA}$
V <sub>OH</sub>	Output HIGH Voltage 10%	6 V <sub>CC</sub> 2.5			V	Min	I <sub>OH</sub> = -1 mA
	5%	V <sub>CC</sub> 2.7			V	IVIIII	$I_{OH} = -1 \text{ mA}$
V <sub>OL</sub>	Output LOW Voltage 10%	√ V <sub>CC</sub>		0.5		Min	I <sub>OL</sub> = 20 mA
I <sub>IH</sub>	Input HIGH Current			5.0	μΑ	Max	V <sub>IN</sub> = 2.7V
I <sub>BVI</sub>	Input HIGH Current Breakdown Te	est		7.0	μΑ	Max	V <sub>IN</sub> = 7.0V
I <sub>CEX</sub>	Output HIGH Leakage Current			50	μΑ	Max	$V_{OUT} = V_{CC}$
V <sub>ID</sub>	Input Leakage Test	4.75			V	0.0	I <sub>ID</sub> = 1.9 μA
		4.73					All other pins grounded
I <sub>OD</sub>	Output Leakage Circuit Current			3.75	μА	0.0	V <sub>IOD</sub> = 150 mV
							All other pins grounded
I <sub>IL</sub>	Input LOW Current			-0.6	mA	Max	V <sub>IN</sub> = 0.5V
Ios	Output Short-Circuit Current	-60		-150	mA	Max	V <sub>OUT</sub> = 0V
I <sub>CCH</sub>	Power Supply Current		12	18	mA	Max	$V_O = HIGH$
I <sub>CCL</sub>	Power Supply Current		18	28	mA	Max	V <sub>O</sub> = LOW

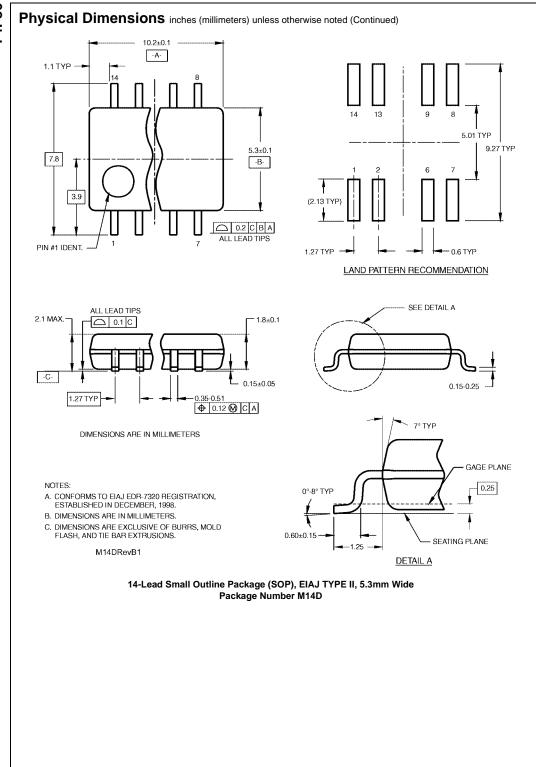
#### **AC Electrical Characteristics**

Symbol	Parameter	$T_A = +25^{\circ}C$ $V_{CC} = +5.0V$ $C_L = 50 \text{ pF}$			$T_A = 0$ °C to $+70$ °C $V_{CC} = +5.0V$ $C_L = 50 \text{ pF}$		Units
		Min	Тур	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay	3.0	4.0	5.5	3.0	6.5	
t <sub>PHL</sub>	A <sub>n</sub> , B <sub>n</sub> to O <sub>n</sub> (Other Input LOW)	3.0	4.2	5.5	3.0	6.5	ns
t <sub>PLH</sub>	Propagation Delay	3.5	5.3	7.0	3.5	8.0	
t <sub>PHL</sub>	A <sub>n</sub> , B <sub>n</sub> to O <sub>n</sub> (Other Input HIGH)	3.0	4.7	6.5	3.0	7.5	ns

M14A (REVIH)

#### Physical Dimensions inches (millimeters) unless otherwise noted $\frac{0.335 - 0.344}{(8.509 - 8.738)}$ $\frac{0.228 - 0.244}{(5.791 - 6.198)}$ LEAD NO. 1 IDENT $\frac{0.010}{(0.254)}$ MAX $\frac{0.150 - 0.157}{(3.810 - 3.988)}$ $\frac{0.053 - 0.069}{(1.346 - 1.753)}$ $\frac{0.010 - 0.020}{(0.254 - 0.508)}$ 8° MAX TYP ALL LEADS $\frac{0.004 - 0.010}{(0.102 - 0.254)}$ SEATING PLANE 0.014 (0.356) $\frac{0.014 - 0.020}{(0.356 - 0.508)} \text{ TYP}$ 0.008 - 0.010 (0.203 - 0.254) TYP ALL LEADS 0.050 (1.270) TYP 0.016 - 0.050 (0.406 - 1.270) TYP ALL LEADS 0.004 (0.102) ALL LEAD TIPS - 0.008 (0.203) TYP

14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow Package Number M14A



#### Physical Dimensions inches (millimeters) unless otherwise noted (Continued) 0.740 - 0.770(18.80 - 19.56)0.090 (2.286) 14 13 12 11 10 9 8 14 13 12 0.250 ± 0.010 PIN NO. 1 IDENT PIN NO. 1 IDENT 1 2 3 4 5 6 7 1 2 3 $\frac{0.092}{(2.337)}$ DIA 0.030 MAX (0.762) DEPTH OPTION 1 OPTION 02 $\frac{0.135 \pm 0.005}{(3.429 \pm 0.127)}$ 0.300 - 0.320 $\overline{(7.620 - 8.128)}$ 0.065 $\frac{0.145 - 0.200}{(3.683 - 5.080)}$ 0.060 4° TYP Optional (1.524) (1.651) $\frac{0.008 - 0.016}{(0.203 - 0.406)}$ TYP 0.020 (0.508) 0.125 - 0.150 $0.075 \pm 0.015$ (3.175 - 3.810)0.280 (1.905 ± 0.381) 0.014-0.023 TYP (7.112) MIN 0.100 ± 0.010 (2.540 ± 0.254) (0.356 - 0.584) $\frac{0.050 \pm 0.010}{(1.270 - 0.254)} \text{ TYP}$ 0.325 <sup>+0.040</sup> -0.015

14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide Package Number N14A

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 $8.255 + 1.016 \\ -0.381$ 

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N144 (REV.E)

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