

9A HIGH-SPEED MOSFET DRIVERS

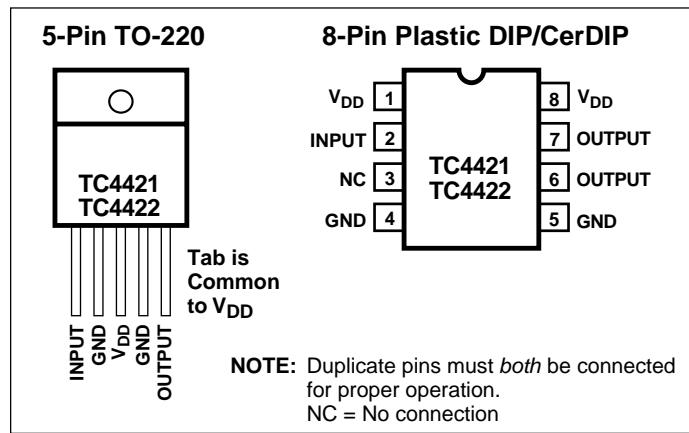
FEATURES

- Tough CMOS™ Construction
- High Peak Output Current 9A
- High Continuous Output Current 2A Max
- Fast Rise and Fall Times:
 - 30nsec with 4,700pF Load
 - 180nsec with 47,000pF Load
- Short Internal Delays 30nsec Typ.
- Low Output Impedance 1.4W Typ.

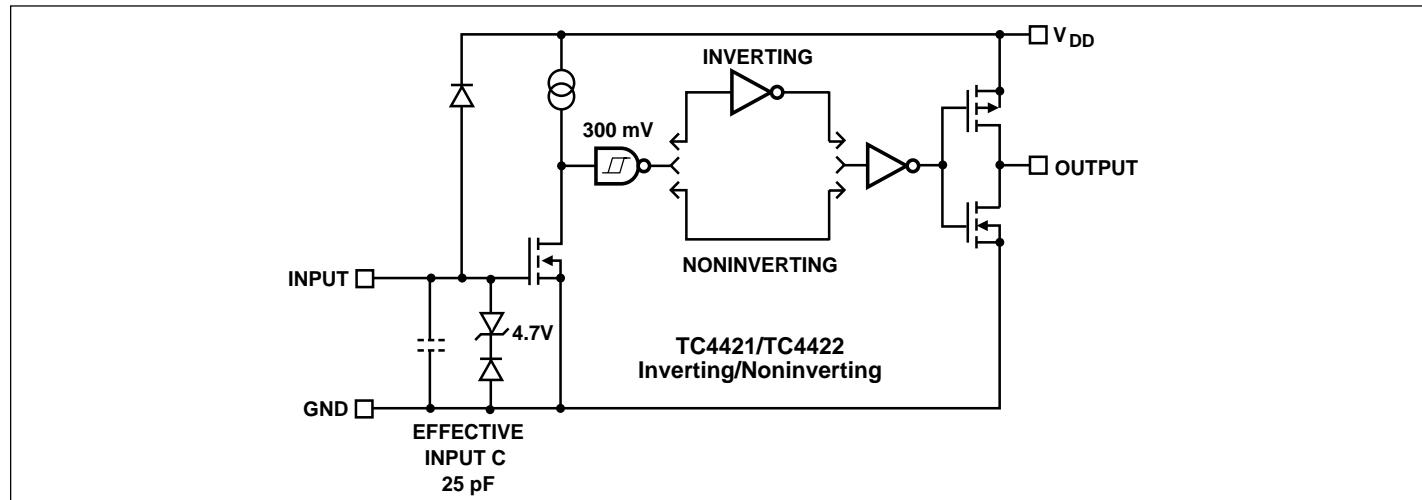
APPLICATIONS

- Line Drivers for Extra-Heavily-Loaded Lines
- Pulse Generators
- Driving the Largest MOSFETs and IGBTs
- Local Power ON/OFF Switch
- Motor and Solenoid Driver

PIN CONFIGURATIONS



FUNCTIONAL BLOCK DIAGRAM



GENERAL DESCRIPTION

The TC4421/4422 are high current buffer/drivers capable of driving large MOSFETs and IGBTs.

They are essentially immune to any form of upset except direct overvoltage or over-dissipation — they cannot be latched under any conditions within their power and voltage ratings; they are not subject to damage or improper operation when up to 5V of ground bounce is present on their ground terminals; they can accept, without either damage or logic upset, more than 1A inductive current of either polarity being forced back into their outputs. In addition, all terminals are fully protected against up to 4kV of electrostatic discharge.

The TC4421/4422 inputs may be driven directly from either TTL or CMOS (3V to 18V). In addition, 300mV of hysteresis is built into the input, providing noise immunity and allowing the device to be driven from slowly rising or falling waveforms.

ORDERING INFORMATION

Part No.	Package	Temperature Range
TC4421CAT	5-Pin TO-220	0°C to +70°C
TC4421CPA	8-Pin PDIP	0°C to +70°C
TC4421EPA	8-Pin PDIP	-40°C to +85°C
TC4421MJA	8-Pin CerDIP	-55°C to +125°C
TC4422CAT	5-Pin TO-220	0°C to +70°C
TC4422CPA	8-Pin PDIP	0°C to +70°C
TC4422EPA	8-Pin PDIP	-40°C to +85°C
TC4422MJA	8-Pin CerDIP	-55°C to +125°C

TC4421
TC4422
ABSOLUTE MAXIMUM RATINGS*Power Dissipation, ($T_A \leq 70^\circ\text{C}$)

PDIP	730W
CerDIP	800mW
5-Pin TO-220	1.6W

Power Dissipation, ($T_A \leq 70^\circ\text{C}$)

5-Pin TO-220 (With Heat Sink)	1.60W
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Derating Factors (To Ambient)

PDIP	8mW/ $^\circ\text{C}$
CerDIP	6.4mW/ $^\circ\text{C}$
5-Pin TO-220	12mW/ $^\circ\text{C}$

Thermal Impedance (To Case)

5-Pin TO-220 R _{QJ-C}	10 $^\circ\text{C}/\text{W}$
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Storage Temperature

Operating Temperature (Chip)

Operating Temperature (Ambient)

C Version	0 $^\circ\text{C}$ to +70 $^\circ\text{C}$
E Version	-40 $^\circ\text{C}$ to +85 $^\circ\text{C}$
M Version	-55 $^\circ\text{C}$ to +125 $^\circ\text{C}$

Lead Temperature (10 sec)

Supply Voltage

Input Voltage

Input Current ($V_{IN} > V_{DD}$)

50mA

*Static-sensitive device. Unused devices must be stored in conductive material. Protect devices from static discharge and static fields. Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operation sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS: $T_A = 25^\circ\text{C}$ with $4.5V \leq V_{DD} \leq 18V$ unless otherwise specified.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Input						
V_{IH}	Logic 1 Input Voltage		2.4	1.8	—	V
V_{IL}	Logic 0 Input Voltage		—	1.3	0.8	V
I_{IN}	Input Current	$0V \leq V_{IN} \leq V_{DD}$	-10	—	10	μA
Output						
V_{OH}	High Output Voltage	See Figure 1	$V_{DD} - 0.025$	—	—	V
V_{OL}	Low Output Voltage	See Figure 1	—	—	0.025	V
R_o	Output Resistance, High	$V_{DD} = 18V, I_O = 10\text{mA}$	—	1.4	—	Ω
R_o	Output Resistance, Low	$V_{DD} = 18V, I_O = 10\text{mA}$	—	0.9	1.7	Ω
I_{PK}	Peak Output Current	$V_{DD} = 18V$	—	9	—	A
I_{DC}	Continuous Output Current	$10V \leq V_{DD} \leq 18V, T_A = 25^\circ\text{C}$ (TC4421/22 CAT only)	2			A
I_{REV}	Latch-Up Protection	Duty Cycle $\leq 2\%$ Withstand Reverse Current	>1500 $t \leq 300\mu\text{sec}$	—	—	mA
Switching Time (Note 1)						
t_R	Rise Time	Figure 1, $C_L = 10,000\text{pF}$	—	60	75	nsec
t_F	Fall Time	Figure 1, $C_L = 10,000\text{pF}$	—	60	75	nsec
t_{D1}	Delay Time	Figure 1	—	30	60	nsec
t_{D2}	Delay Time	Figure 1	—	33	60	nsec
Power Supply						
I_S	Power Supply Current	$V_{IN} = 3V$	—	0.2	1.5	mA
		$V_{IN} = 0V$	—	55	150	μA
V_{DD}	Operating Input Voltage		4.5	—	18	V
Input						
V_{IH}	Logic 1 Input Voltage		2.4	—	—	V
V_{IL}	Logic 0 Input Voltage		—	—	0.8	V
I_{IN}	Input Current	$0V \leq V_{IN} \leq V_{DD}$	-10	—	10	μA

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TC4421
TC4422

ELECTRICAL CHARACTERISTICS: Measured over operating temperature range with $4.5V \leq V_S \leq 18V$, unless otherwise specified.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Input						
V_{IH}	Logic 1 Input Voltage		2.4	—	—	V
V_{IL}	Logic 0 Input Voltage		—	—	0.8	V
I_{IN}	Input Current	$0V \leq V_{IN} \leq V_{DD}$	-10	—	10	μA
Output						
V_{OH}	High Output Voltage	See Figure 1	$V_{DD} - 0.025$	—	—	V
V_{OL}	Low Output Voltage	See Figure 1	—	—	0.025	V
R_O	Output Resistance, High	$V_{DD} = 18V, I_O = 10mA$	—	2.4	3.6	W
R_O	Output Resistance, Low	$V_{DD} = 18V, I_O = 10mA$	—	1.8	2.7	W
Switching Time (Note 1)						
t_R	Rise Time	Figure 1, $C_L = 10,000pF$	—	60	120	nsec
t_F	Fall Time	Figure 1, $C_L = 10,000pF$	—	60	120	nsec
t_{D1}	Delay Time	Figure 1	—	50	80	nsec
t_{D2}	Delay Time	Figure 1	—	65	80	nsec
Power Supply						
I_S	Power Supply Current	$V_{IN} = 3V$ $V_{IN} = 0V$	—	0.45	3	mA
V_{DD}	Operating Input Voltage		4.5	—	18	V

NOTE: 1. Switching times guaranteed by design.

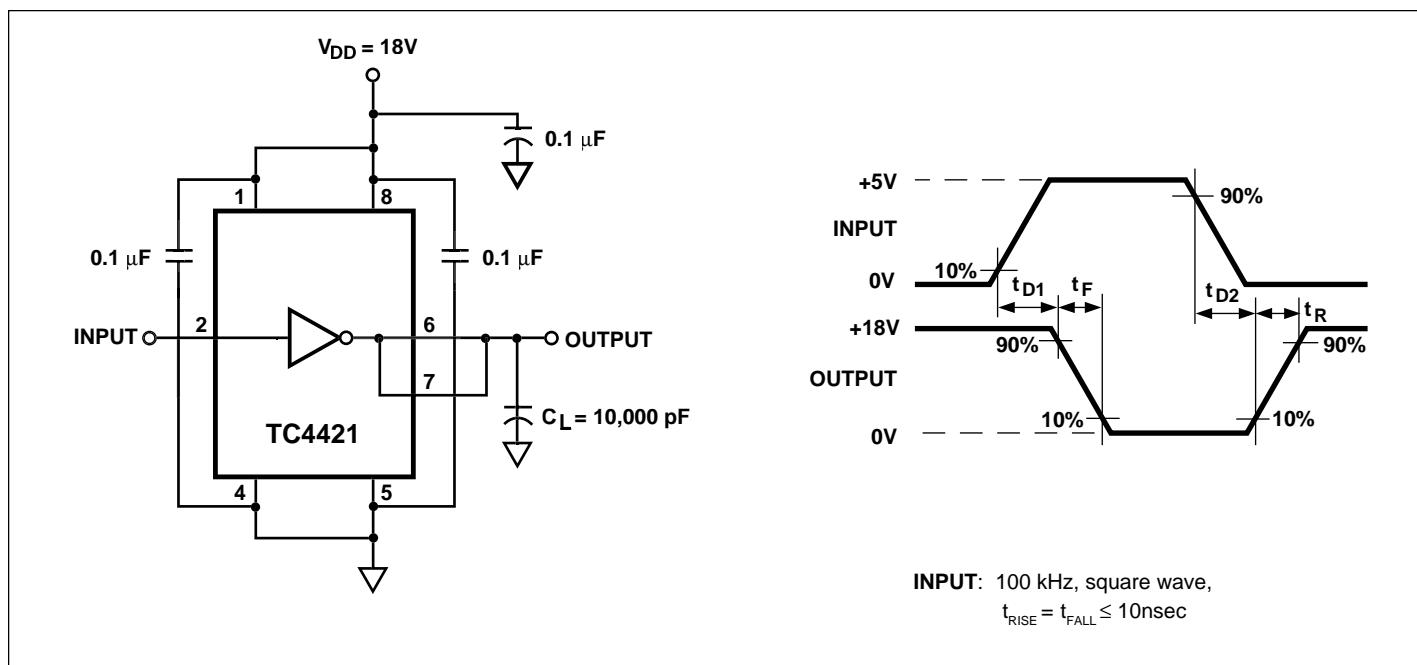
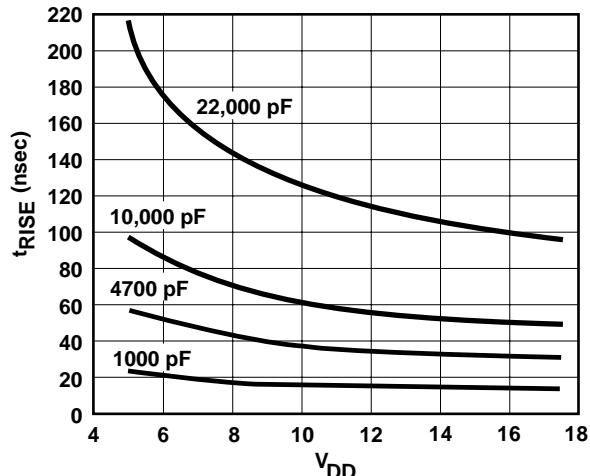


Figure 1. Switching Time Test Circuit

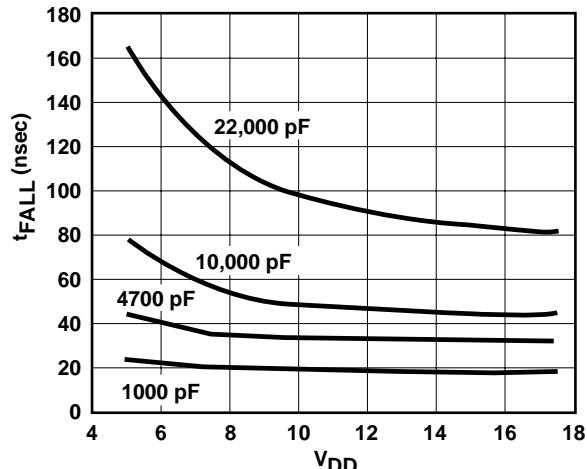
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TYPICAL CHARACTERISTICS

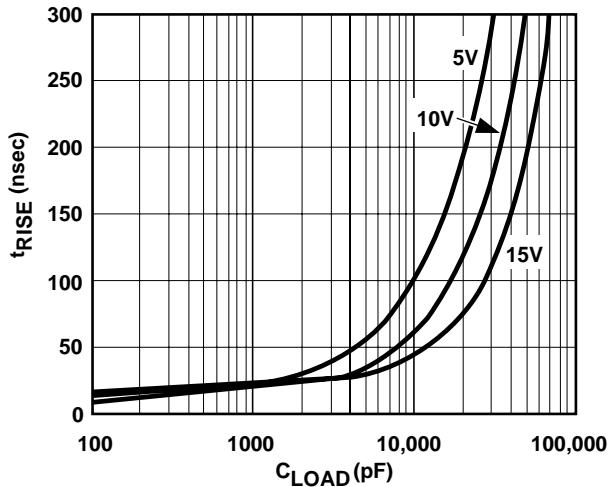
Rise Time vs. Supply Voltage



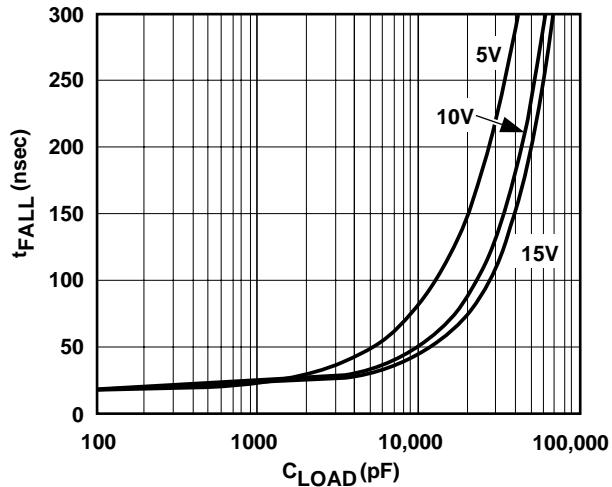
Fall Time vs. Supply Voltage



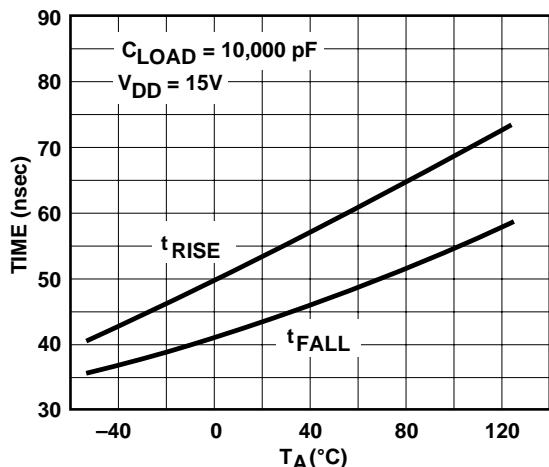
Rise Time vs. Capacitive Load



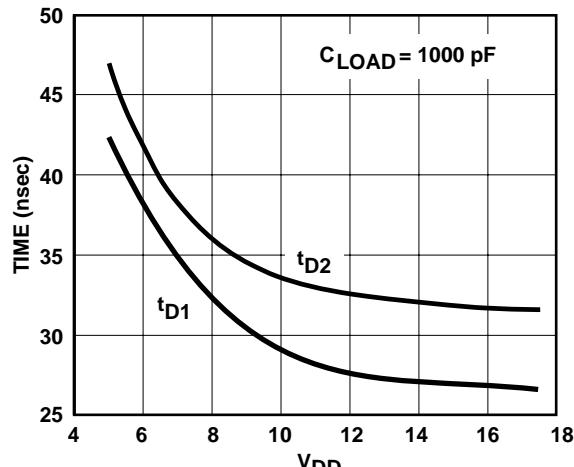
Fall Time vs. Capacitive Load



Rise and Fall Times vs. Temperature



Propagation Delay vs. Supply Voltage

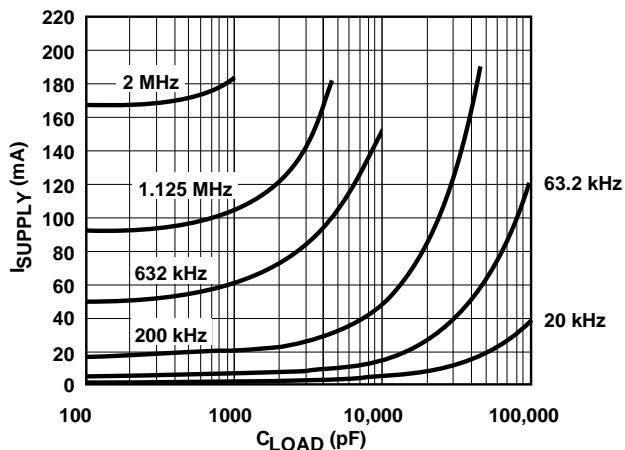


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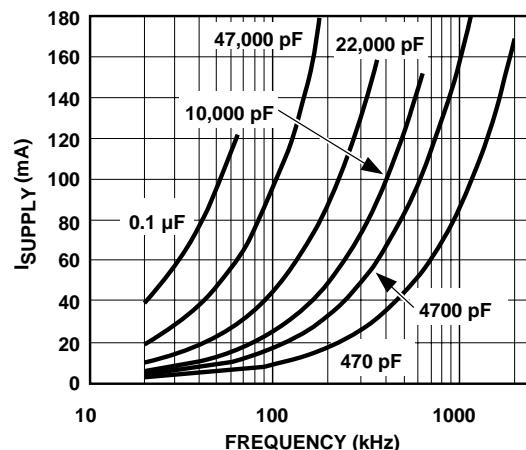
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TYPICAL CHARACTERISTICS (CONT.)

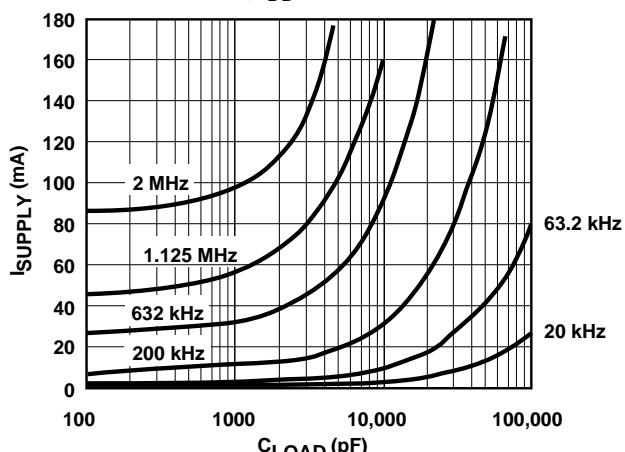
Supply Current vs. Capacitive Load
($V_{DD} = 18V$)



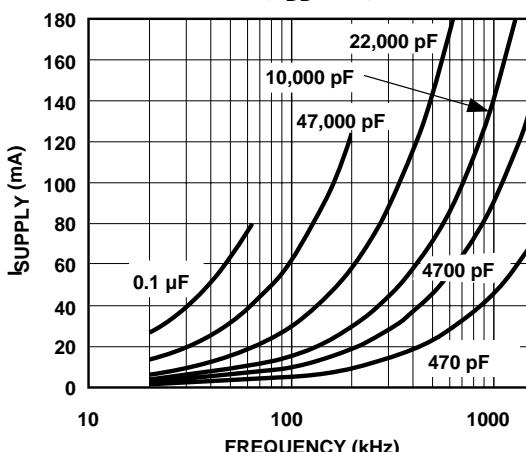
Supply Current vs. Frequency
($V_{DD} = 18V$)



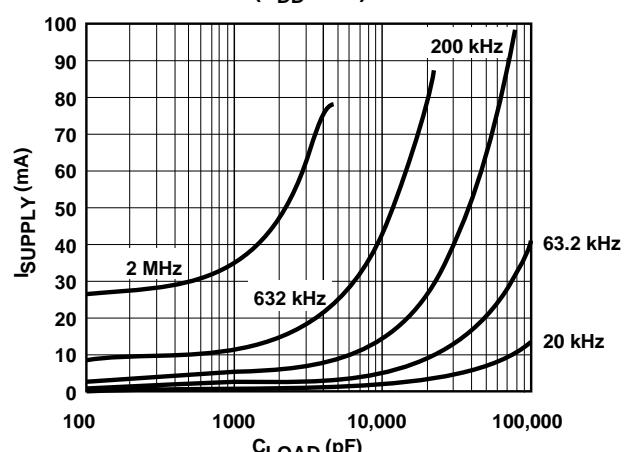
Supply Current vs. Capacitive Load,
($V_{DD} = 12$)



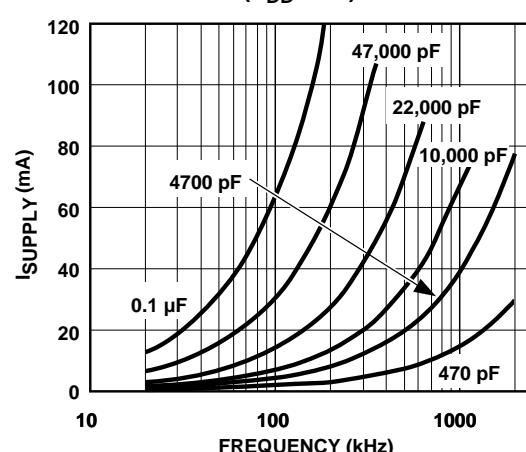
Supply Current vs. Frequency
($V_{DD} = 12$)



Supply Current vs. Capacitive Load
($V_{DD} = 6V$)



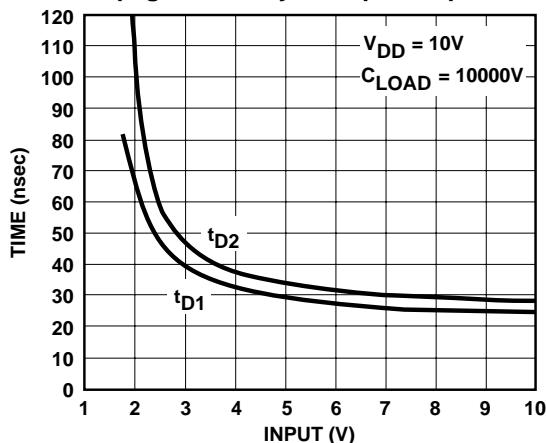
Supply Current vs. Frequency
($V_{DD} = 6V$)



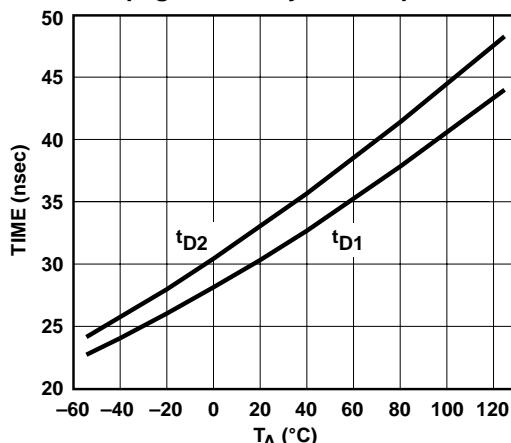
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TYPICAL CHARACTERISTICS (Cont.)

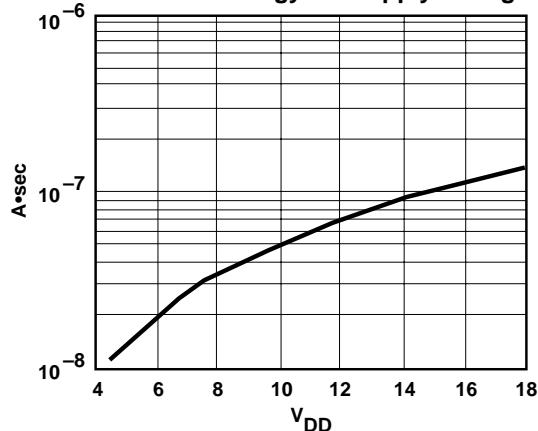
Propagation Delay vs. Input Amplitude



Propagation Delay vs. Temperature

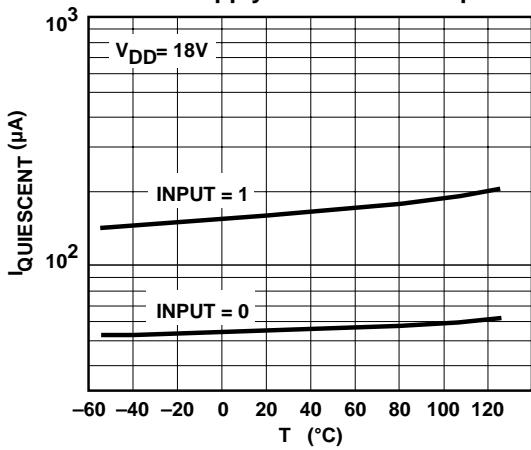


Crossover Energy vs. Supply Voltage

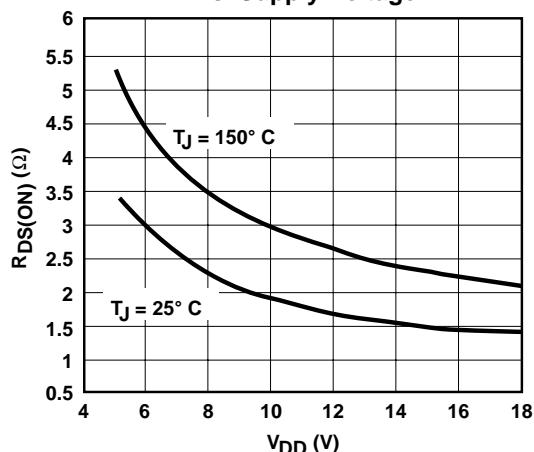


NOTE: The values on this graph represent the loss seen by the driver during a complete cycle. For the loss in a single transition, divide the stated value by 2.

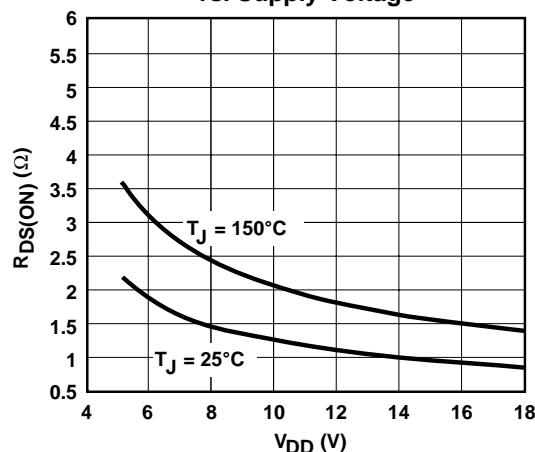
Quiescent Supply Current vs. Temperature



High-State Output Resistance vs. Supply Voltage



Low-State Output Resistance vs. Supply Voltage

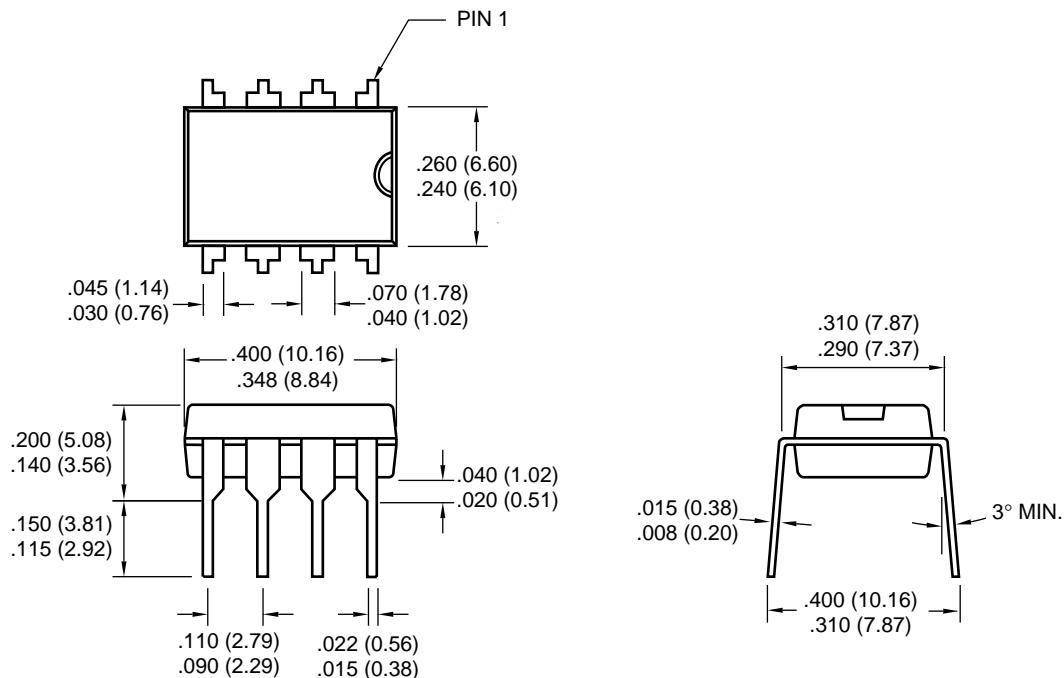


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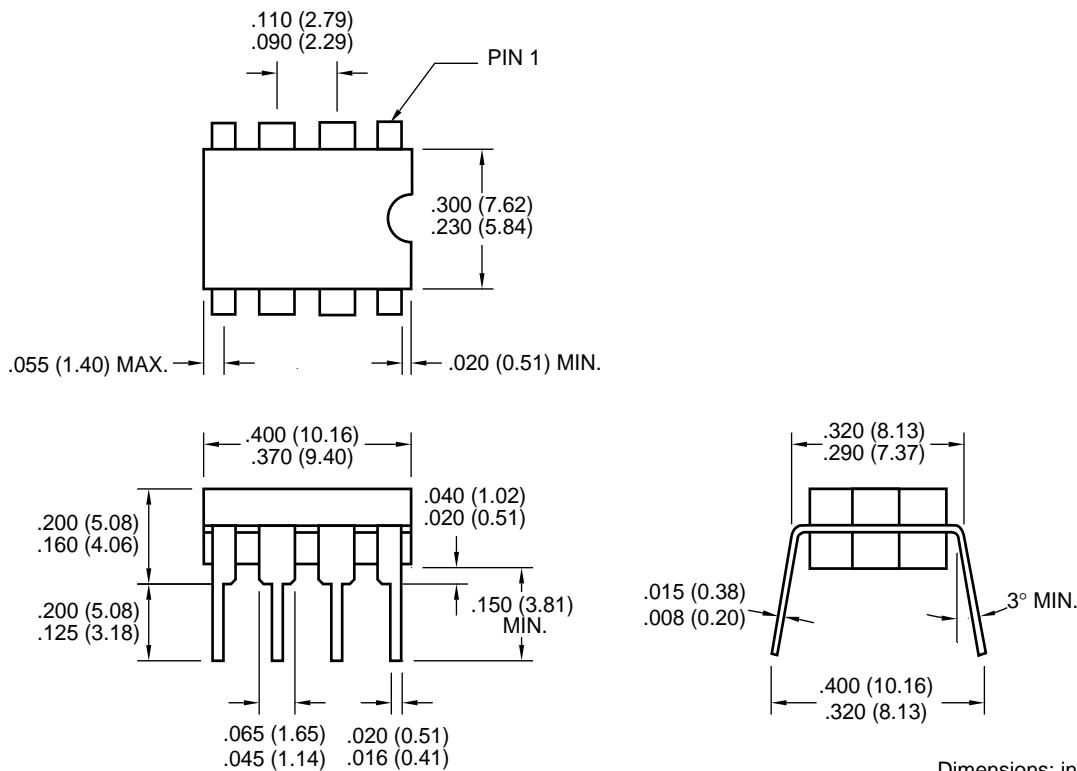
TC4421
TC4422

PACKAGE DIMENSIONS

8-Pin Plastic DIP



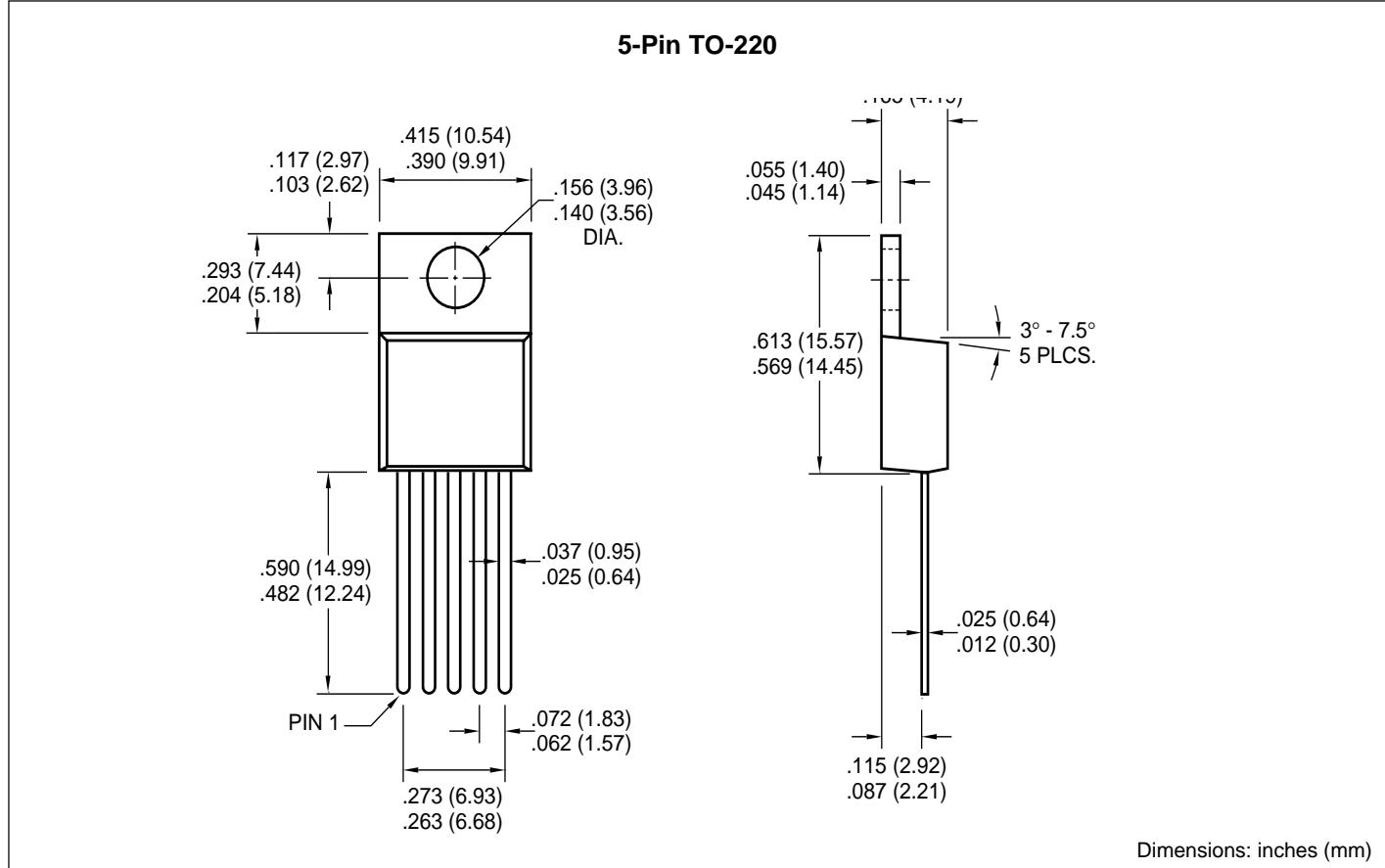
8-Pin CerDIP



Dimensions: inches (mm)

TC4421
TC4422

PACKAGE DIMENSIONS (Cont.)





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Corporate Office

2355 West Chandler Blvd.
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Dallas

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Addison, TX 75001
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New York

150 Motor Parkway, Suite 202
Hauppauge, NY 11788
Tel: 631-273-5305 Fax: 631-273-5335

San Jose

Microchip Technology Inc.
2107 North First Street, Suite 590
San Jose, CA 95131
Tel: 408-436-7950 Fax: 408-436-7955

Toronto

6285 Northam Drive, Suite 108
Mississauga, Ontario L4V 1X5, Canada
Tel: 905-673-0699 Fax: 905-673-6509

ASIA/PACIFIC

China - Beijing

Microchip Technology Beijing Office
Unit 915
New China Hong Kong Manhattan Bldg.
No. 6 Chaoyangmen Beidajie
Beijing, 100027, No. China
Tel: 86-10-85282100 Fax: 86-10-85282104

China - Shanghai

Microchip Technology Shanghai Office
Room 701, Bldg. B
Far East International Plaza
No. 317 Xian Xia Road
Shanghai, 200051
Tel: 86-21-6275-5700 Fax: 86-21-6275-5060

Hong Kong

Microchip Asia Pacific
RM 2101, Tower 2, Metroplaza
223 Hing Fong Road
Kwai Fong, N.T., Hong Kong
Tel: 852-2401-1200 Fax: 852-2401-3431

India

Microchip Technology Inc.
India Liaison Office
Divyasree Chambers
1 Floor, Wing A (A3/A4)
No. 11, OIShaungessey Road
Bangalore, 560 025, India
Tel: 91-80-2290061 Fax: 91-80-2290062

Japan

Microchip Technology Intl. Inc.
Benex S-1 6F
3-18-20, Shinyokohama
Kohoku-Ku, Yokohama-shi
Kanagawa, 222-0033, Japan
Tel: 81-45-471- 6166 Fax: 81-45-471-6122

Korea

Microchip Technology Korea
168-1, Youngbo Bldg. 3 Floor
Samsung-Dong, Kangnam-Ku
Seoul, Korea
Tel: 82-2-554-7200 Fax: 82-2-558-5934

ASIA/PACIFIC (continued)

Singapore

Microchip Technology Singapore Pte Ltd.
200 Middle Road
#07-02 Prime Centre
Singapore, 188980
Tel: 65-334-8870 Fax: 65-334-8850

Taiwan

Microchip Technology Taiwan
11F-3, No. 207
Tung Hua North Road
Taipei, 105, Taiwan
Tel: 886-2-2717-7175 Fax: 886-2-2545-0139

EUROPE

Australia

Microchip Technology Australia Pty Ltd
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Australia
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Denmark

Microchip Technology Denmark ApS
Regus Business Centre
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Tel: 45 4420 9895 Fax: 45 4420 9910

France

Arizona Microchip Technology SARL
Parc d'Activite du Moulin de Massy
43 Rue du Saule Trapu
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91300 Massy, France
Tel: 33-1-69-53-63-20 Fax: 33-1-69-30-90-79

Germany

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Germany

Analog Product Sales
Lochamer Strasse 13
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Italy

Arizona Microchip Technology SRL
Centro Direzionale Colleoni
Palazzo Taurus 1 V. Le Colleoni 1
20041 Agrate Brianza
Milan, Italy
Tel: 39-039-65791-1 Fax: 39-039-6899883

United Kingdom

Arizona Microchip Technology Ltd.
505 Eskdale Road
Winnersh Triangle
Wokingham
Berkshire, England RG41 5TU
Tel: 44 118 921 5869 Fax: 44-118 921-5820

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