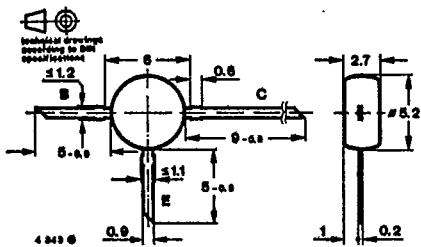


**Silicon PNP Planar RF Transistor**

Applications: RF amplifier up to GHz range specially for wide band antenna amplifier

**Features:**

- High power gain
- Low noise figures
- High transition frequency

**Dimensions in mm**

Plastic case  
 ~ 50 B 3 DIN 41867  
 ~ JEDEC TO 50  
 Weight max. 0.25 g

**Absolute maximum ratings**

Collector-base voltage	$-V_{CBO}$	15	V
Collector-emitter voltage	$-V_{CEO}$	15	V
Emitter-base voltage	$-V_{EBO}$	3	V
Collector current	$-I_C$	75	mA
Collector peak current	$-I_{CM}$	150	mA
Total power dissipation $T_{amb} \leq 60^\circ\text{C}$	$P_{tot}$	500	mW
Junction temperature	$T_J$	150	$^\circ\text{C}$
Storage temperature range	$T_{stg}$	-55 ... +150	$^\circ\text{C}$

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**BFT 96****Thermal resistances**

Junction ambient  
 $I = 8 \text{ mm}$ ,  $T_L = \text{konstant}$   
 mounted on a glass fiber board  
 $30 \times 30 \times 1.5 \text{ mm}^3$

	Min.	Typ.	Max.
$R_{\text{thJA}}$		270	K/W

**DC characteristics**

$T_j = 25^\circ\text{C}$ , unless otherwise specified

Collector cut-off current $-V_{CB} = 10 \text{ V}$	$-I_{CBO}$		100	nA
Collector-base breakdown voltage $-I_C = 100 \mu\text{A}$	$-V_{(\text{BR})CBO}$	15		V
Collector-emitter breakdown voltage $-I_C = 5 \text{ mA}$	$-V_{(\text{BR})CEO}^{1)}$	15		V
Emitter-base breakdown voltage $-I_E = 10 \mu\text{A}$	$-V_{(\text{BR})EBO}$	3		V
DC forward current transfer ratio $-V_{CE} = 5 \text{ V}$ , $-I_C = 50 \text{ mA}$	$h_{FE}$	80		

**AC characteristics**

$T_{\text{amb}} = 25^\circ\text{C}$

Gain bandwidth product $-V_{CB} = 10 \text{ V}$ , $I_C = 50 \text{ mA}$ , $f = 500 \text{ MHz}$	$f_T$	5	GHz
Collector-base capacitance $-V_{CB} = 1 \text{ V}$ , $f = 1 \text{ MHz}$	$C_{CBO}$	2.6	pF
$-V_{CB} = 10 \text{ V}$ , $f = 100 \text{ MHz}$	$C_{CBO}$	1.2	pF
Noise figure $-V_{CE} = 10 \text{ V}$ , $I_C = 50 \text{ mA}$ , $R_G = R_{\text{Gopt}}$ $f = 500 \text{ MHz}$	$F$	3.3	dB
$f = 1 \text{ GHz}$	$F$	4	dB
Power gain $-V_{CB} = 10 \text{ V}$ , $I_C = 50 \text{ mA}$ , $f = 1 \text{ GHz}$	$G_{pb}$	10	dB
Forward transmission factor $-V_{CE} = 10 \text{ V}$ , $-I_C = 50 \text{ mA}$ , $f = 1 \text{ GHz}$ ,	$ S_{21e} ^2$	9	dB

<sup>1)</sup>  $\frac{t_p}{T} = 0.01$ ,  $t_p = 0.3 \text{ ms}$

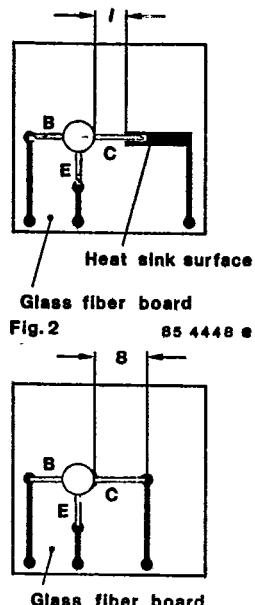
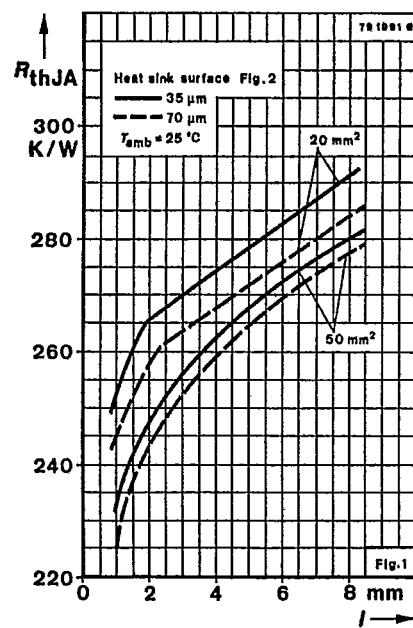
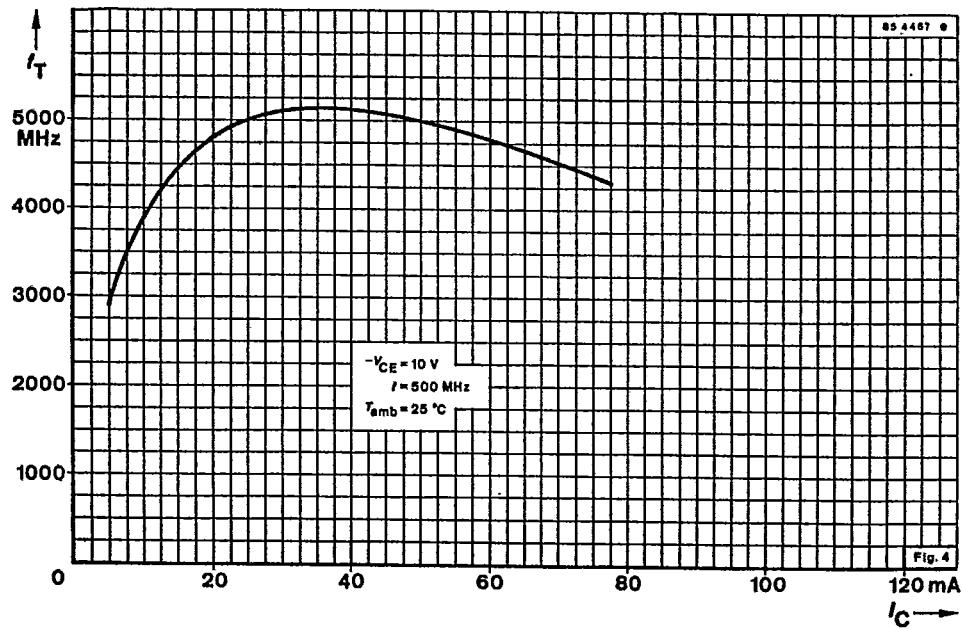
7-31-21  
BFT 96

Fig. 3 85 3884 e



## 7. Taping and Reeling

*T-91-20*

### 7.1. Taping of TO-92 Transistors

Standard reeling: Taped on reel, reeled together with a paper film.

#### 7.1.1. Order Numbers

Add the taping-code to the order number.

#### Example:

Order-No. of Type	BC 238 C	DU	06	Z
Code for TO-92 Transistors				
Orientation of transistor on tape <sup>1)</sup>				
Additional marking for specials <sup>2)</sup>				

<sup>1)</sup> 06 = View on flat side of transistor, view on gummed tape

05 = View on round side of transistor, view on gummed tape

<sup>2)</sup> Additional marking "0": taping without paper film

Additional marking "Z": Zigzag folded tape in special box. Marking for orientation of transistor not necessary, because box can be opened on top or bottom

Example for order No.: BC 237 C DU Z

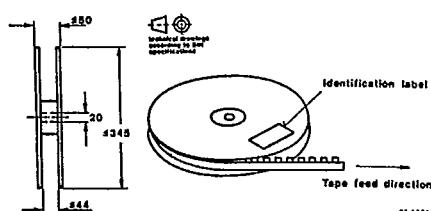


Fig. 7.1. Dimensions of reel in mm

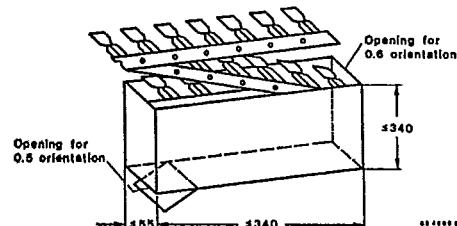


Fig. 7.2. Dimension of box for Zigzag folding in mm

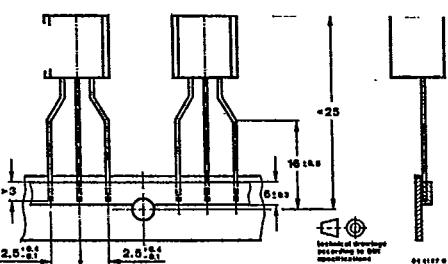


Fig. 7.3 Dimensions of tape in mm

### 7.1.2. Quantity of devices

1000 devices per reel

2000 devices per folded tape in special box.

### 7.2. Taped transistors in SOT 23 and SOT 143 case

#### 7.2.1. Designation

##### a) Standard taping

Designation is attached with code GS 08 in case of standard taping. Example for normal version transistors as standard taped: BF 569-GS 08.

Example for R-version transistors as standard taped: BF 569 R-GS 08.

In case of standard taping, the transistor orientation on the tape is shown in Fig. 7.4 and Fig. 7.5.

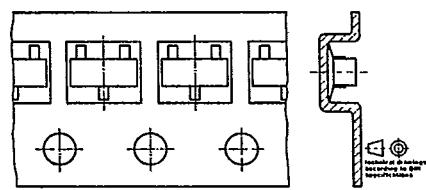


Fig. 7.4 Standard taped SOT 23

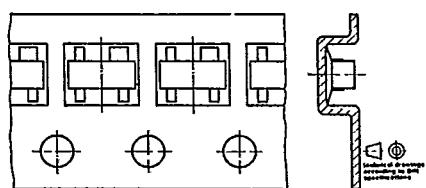


Fig. 7.5. Standard taped SOT 143

T-91-20

## b) Reverse taping

Designation is attached with code GS 07 in case of reverse taping. Example for normal version transistors as reverse taped: BF 569-GS 07.

Example for R-version transistors as reverse taping: BF 569 R-GS 07.

In case of reverse taping, the transistor orientation on the tape is shown in Fig. 7.6.

Regarding MOS-FET and MES-FET devices, reverse taping is at present not available.

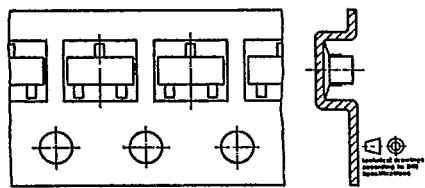


Fig. 7.6 Reverse taped SOT 23

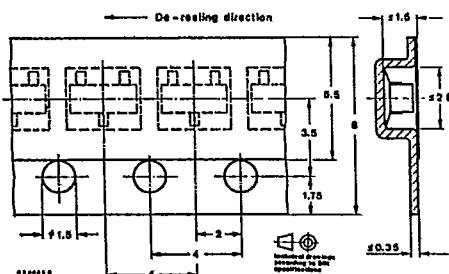


Fig. 7.7 Dimensions of tape in mm

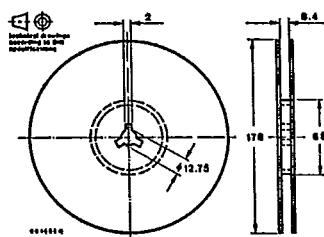


Fig. 7.8 Dimensions of reel in mm

## 7.2.2 Quantity of devices

3000 devices per reel