SN5440, SN54LS40, SN54S40 SN7440, SN74LS40, SN74S40 DUAL 4-INPUT POSITIVE-NAND BUFFERS SDLS108 – APRIL 1985 – REVISED MARCH 1988

- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages in Addition to Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

description

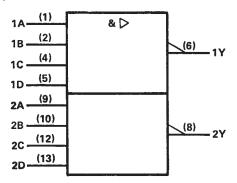
These devices contain two independent 4-input NAND buffer gates.

The SN5440, SN54LS40, and SN54S40 are characterized for operation over the full military temperature range of -55 °C to 125 °C. The SN7440, SN74LS40, and SN74S40 are characterized for operation from 0 °C to 70 °C.

FUNCTION TABLE (each gate)

	INP	UTS		OUTPUT
A	В	С	D	Y
н	н	н	н	L
L	X	х	X	н
X	L	х	X	н
X	х	L	X	н
x	Х	х	L	н

logic symbol[†]



[†]This symbol is in accordance with ANSI/IEEE Std. 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, N, and W packages.

SN5440 ... J PACKAGE SN54LS40, SN54S40 ... J OR W PACKAGE SN7440 ... N PACKAGE SN74LS40, SN74S40 ... D OR N PACKAGE

(TOP VIEW)

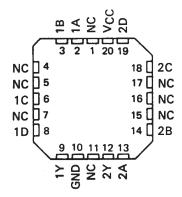
1A [1	14 V _{CC}
1B [2	13 2D
NC 3	12 2C
1C 4	11 NC
1D 5	10 2B
1Y [6]	9 2A
1Y [6	9] 2A
GND [7	8] 2Y

SN5440	W PACKAGE
(TOP	VIEW)
	U 14] 1D

1Y 🗌	2	13	门1C
NC 🗆	3	12] 1B
ˈcc 🏼	4	11	
	5	10	2Y
2A [6	9	2D
2B 🗌	7	8] 2C

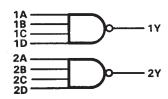
V

SN54LS40, SN54S40 . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

logic diagram



positive logic

 $Y = \overline{A \cdot B \cdot C \cdot D} \text{ or } Y = \overline{A} + \overline{B} + \overline{C} + \overline{D}$

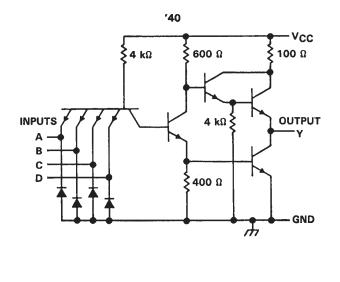
PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

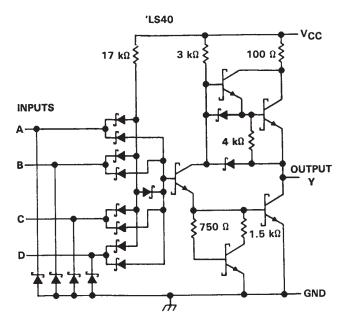


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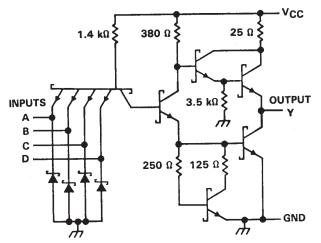
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schematics (each gate)





'S40



Resistor values shown are nominal.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC} (see Note 1)	
Input voltage: '40, 'S40	
Operating free-air temperature range: SN54'	– 55 °C to 125 °C
Storage temperature range	

NOTE 1: Voltage values are with respect to network ground terminal.



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recommended operating conditions

			SN5440			SN7440			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	V	
VIH	High-level input voltage	2			2			V	
VIL	Low-level input voltage			0.8			0.8	V	
юн	High-level output current			- 1.2			- 1.2	mA	
IOL	Low-level output current			48			48	mA	
TA	Operating free-air temperature	- 55		125	0		70	°C	

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS [†]				UNIT				
		TEST CONDIT		MIN	TYP‡	MAX	MIN	TYP‡	MAX	
Viк	V _{CC} = MIN,	l _l = – 12 mA				- 1.5			- 1.5	V
Voн	V _{CC} = MIN,	V _{1L} = 0.8 V,	l _{OH} = 1.2 mA	2.4	3.3		2.4	3.3		V
VOL	V _{CC} = MIN,	V _{IH} = 2 V,	IOL = 48 mA		0.2	0.4		0.2	0.4	V
IL.	V _{CC} = MAX,	V _I = 5.5 V				1			1	mA
ЧН	V _{CC} = MAX,	VI = 2.4 V				40			40	μA
μL	V _{CC} = MAX,	V _I = 0.4 V				- 1.6			- 1.6	mA
IOS§	V _{CC} = MAX			- 20		- 70	- 18		- 70	mA
Іссн	V _{CC} = MAX,	V1 = 0			4	8		4	8	mA
ICCL	V _{CC} = MAX,	V _I = 4.5 V			17	27		17	27	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

[‡] All typical values are at $V_{CC} = 5 V$, $T_A = 25^{\circ}C$. § Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed 100 milliseconds.

switching characteristics, $V_{CC} = 5 V$, $T_A = 25^{\circ}C$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIO	MIN	ТҮР	мах	UNIT	
^t PLH	A 614	~	P 122 O	0. = 15 = 5		13	22	រាន
^t PHL	Any	Т	R _L = 133 Ω,	C _L = 15 pF		8	15	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



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recommended operating conditions

		S	SN54LS40			SN74LS40			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	V	
	High-level input voltage	2			2			V	
	Low-level input voltage			0.7			08	V	
	High-level output current			- 1.2			- 1.2	mA	
¹ OL	Low-level output current			12			24	mA	
TA	Operating free-air temperature	- 55		125	0		70	°C	

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

				s	SN54LS40			SN74LS40			
PARAMETER	TEST CONDITIONS T		MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT		
VIK	V _{CC} = MIN,	l _l = – 18 mA				- 1.5			- 1.5	V	
VOH	V _{CC} = MIN,	VIL = MAX,	I _{OH} = - 1.2 mA	2.5	3.4		2.7	3.4		V	
	V _{CC} = MIN,	V _{IH} = 2 V,	I _{OL} = 12 mA		0.25	0.4		0.25	0.4	v	
VOL	V _{CC} = MIN,	V _{IH} = 2 V,	IOL = 24 mA					0.35	0.5		
li i	V _{CC} = MAX,	V _I = 7 V				0.1			0.1	mA	
Чн	V _{CC} = MAX,	V _I = 2.7 V				20			20	μA	
ΗL	V _{CC} = MAX,	V _I = 0.4 V	<u> </u>			- 0.4			- 0.4	mA	
I _{OS} §	V _{CC} = MAX			- 30		- 130	- 30		- 130	mA	
ІССН	V _{CC} = MAX,	V = 0			0.45	1		0.45	1	mA	
ICCL	V _{CC} = MAX,	V ₁ = 4.5 V			3	6		3	6	mA	

t For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

[‡] All typical values are at $V_{CC} = 5 V$, $T_A = 25^{\circ}C$. §Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.

switching characteristics, $V_{CC} = 5 V$, $T_A = 25^{\circ}C$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS			түр	MAX	UNIT
^t PLH	A	~	$P_{1} = 667 O$	$C_1 = 45 \text{ pF}$		12	24	ns
^t PHL	Any	T	R _L = 667 Ω,	C _L = 45 pF		12	24	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



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recommended operating conditions

			SN54S40			SN74S40			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT	
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	v	
VIH	High-level input voltage	2			2			V	
VIL	Low-level input voltage			0.8			0.8	V	
юн	High-level output current			- 3			- 3	mA	
IOL	Low-level output current			60			60	mA	
TA	Operating free-air temperature	- 55		125	0	<u>.</u>	70	°C	

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS T			5	SN54S40		SN74S40			
				MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT
VIK	V _{CC} = MIN,	l _l = – 18 mA				- 1.2			- 1.2	V
VOH	V _{CC} = MIN,	VIL = 0.8 V,	1 _{OH} = - 3 mA	2.5	3.4		2.7	3.4		V
VOL	V _{CC} = MIN,	V _{IH} = 2 V,	I _{OL} = 60 mA			0.5			0.5	V
4	V _{CC} = MAX,	V _I = 5.5 V				1			1	mA
Чн	V _{CC} = MAX,	V ₁ = 2.7 V				0.1			0.1	mA
հե	V _{CC} = MAX,	V _I = 0.5 V			***	- 4			- 4	mA
los§	V _{CC} = MAX			- 50		- 225	- 50		- 225	mA
1ссн	V _{CC} = MAX,	V _I = 0			10	18		10	18	mA
ICCL	V _{CC} = MAX,	V ₁ = 4.5 V			25	44		25	44	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

‡ All typical values are at $V_{CC} = 5 V$, $T_A = 25^{\circ}C$.

\$ Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed 100 milliseconds.

switching characteristics, $V_{CC} = 5 V$, $T_A = 25^{\circ}C$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	MIN TYP	MAX	UNIT	
^t PLH	^t PLH		RL = 93 Ω,	CL = 50 pF	4	6.5	ns
^t ₽HL	Any	Y	nL - 33 44,	C[= 30 pi	4	6.5	ns
^t PLH			R _L = 93 Ω,	CL = 150 pF	6		ns
^t PHL				CL = 100 pr	6		ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



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