SGS-THOMSON MICROELECTRONICS

IRFZ40
IRFZ40FI

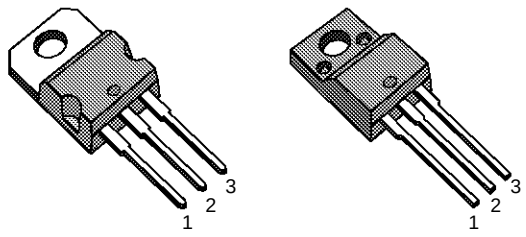
N - CHANNEL ENHANCEMENT MODE POWER MOS TRANSISTORS

TYPE	V_{DSS}	$R_{DS(on)}$	I_D
IRFZ40	50 V	< 0.028 Ω	50 A
IRFZ40FI	50 V	< 0.028 Ω	27 A

- TYPICAL $R_{DS(on)}$ = 0.022 Ω
- AVALANCHE RUGGED TECHNOLOGY
- 100% AVALANCHE TESTED
- REPETITIVE AVALANCHE DATA AT 100°C
- LOW GATE CHARGE
- HIGH CURRENT CAPABILITY
- 175°C OPERATING TEMPERATURE

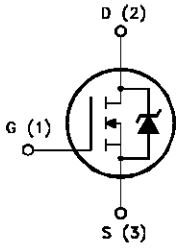
APPLICATIONS

- HIGH CURRENT, HIGH SPEED SWITCHING
- SOLENOID AND RELAY DRIVERS
- REGULATORS
- DC-DC & DC-AC CONVERTERS
- MOTOR CONTROL, AUDIO AMPLIFIERS
- AUTOMOTIVE ENVIRONMENT (INJECTION, ABS, AIR-BAG, LAMPDRIVERS, Etc.)



TO-220 **ISOWATT220**

INTERNAL SCHEMATIC DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value		Unit
		IRFZ40	IRFZ40FI	
V_{DS}	Drain-source Voltage (V_{GS} = 0)	50	50	V
V_{DGR}	Drain- gate Voltage (R_{GS} = 20 kΩ)	50	50	V
V_{GS}	Gate-source Voltage	± 20		V
I_D	Drain Current (cont.) at T_c = 25 °C	50	27	A
I_D	Drain Current (cont.) at T_c = 100 °C	35	19	A
I_{DM}(•)	Drain Current (pulsed)	200	200	A
P_{tot}	Total Dissipation at T_c = 25 °C	150	45	W
	Derating Factor	1	0.3	W/°C
V_{ISO}	Insulation Withstand Voltage (DC)	—	2000	V
T_{stg}	Storage Temperature	-65 to 175		°C
T_j	Max. Operating Junction Temperature	175		°C

(•) Pulse width limited by safe operating area

THERMAL DATA

			TO-220	ISOWATT220	
$R_{thj\text{-}case}$	Thermal Resistance Junction-case	Max	1	3.33	°C/W
$R_{thj\text{-}amb}$	Thermal Resistance Junction-ambient	Max	62.5		°C/W
$R_{thc\text{-}s}$	Thermal Resistance Case-sink	Typ	0.5		°C/W
T_l	Maximum Lead Temperature For Soldering Purpose		300		°C

AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I_{AR}	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T_j max, $\delta < 1\%$)	50	A
E_{AS}	Single Pulse Avalanche Energy (starting $T_j = 25\ ^oC$, $I_D = I_{AR}$, $V_{DD} = 25$ V)	400	mJ
E_{AR}	Repetitive Avalanche Energy (pulse width limited by T_j max, $\delta < 1\%$)	100	mJ
I_{AR}	Avalanche Current, Repetitive or Not-Repetitive ($T_c = 100\ ^oC$, pulse width limited by T_j max, $\delta < 1\%$)	35	A

ELECTRICAL CHARACTERISTICS ($T_{case} = 25\ ^oC$ unless otherwise specified)

OFF

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{(BR)DSS}$	Drain-source Breakdown Voltage	$I_D = 250\ \mu A$ $V_{GS} = 0$	50			V
I_{DSS}	Zero Gate Voltage Drain Current ($V_{GS} = 0$)	V_{DS} = Max Rating V_{DS} = Max Rating x 0.8 $T_c = 125\ ^oC$			250 1000	μA μA
I_{GSS}	Gate-body Leakage Current ($V_{DS} = 0$)	$V_{GS} = \pm 20$ V			± 100	nA

ON (*)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$ $I_D = 250\ \mu A$	2	2.9	4	V
$R_{DS(on)}$	Static Drain-source On Resistance	$V_{GS} = 10$V $I_D = 29$ A		0.022	0.028	Ω
$I_{D(on)}$	On State Drain Current	$V_{DS} > I_{D(on)} \times R_{DS(on)max}$ $V_{GS} = 10$ V	50			A

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
g_{fs} (*)	Forward Transconductance	$V_{DS} > I_{D(on)} \times R_{DS(on)max}$ $I_D = 29$ A	17	22		S
C_{iss} C_{oss} C_{rss}	Input Capacitance Output Capacitance Reverse Transfer Capacitance	$V_{DS} = 25$ V f = 1 MHz $V_{GS} = 0$		1700 630 200	2200 850 260	pF pF pF

ELECTRICAL CHARACTERISTICS (continued)

SWITCHING RESISTIVE LOAD

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on Time	V_{DD} = 25 V I_D = 29 A		50	70	ns
t_r	Rise Time	R_G = 4.7 Ω V_{GS} = 10 V		110	160	ns
$t_{d(off)}$	Turn-off Delay Time	(see test circuit)		60	90	ns
t_f	Fall Time			25	35	ns
Q_g	Total Gate Charge	I_D = 64 A V_{GS} = 10 V		50	70	nC
Q_{gs}	Gate-Source Charge	V_{DD} = Max Rating x 0.8		15		nC
Q_{gd}	Gate-Drain Charge	(see test circuit)		27		nC

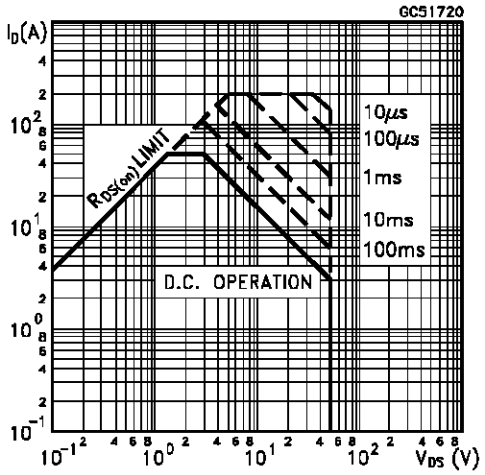
SOURCE DRAIN DIODE

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
I_{SD}	Source-drain Current				50	A
I_{SDM}(•)	Source-drain Current (pulsed)				200	A
V_{SD} (*)	Forward On Voltage	V_{GS} = 0 I_{SD} = 50 A			2	V
t_{rr}	Reverse Recovery Time	I_{SD} = 50 A di/dt = 100 A/µs V_{DD} = 30 V T_j = 150 °C		150		ns
Q_{rr}	Reverse Recovery Charge			0.45		µC

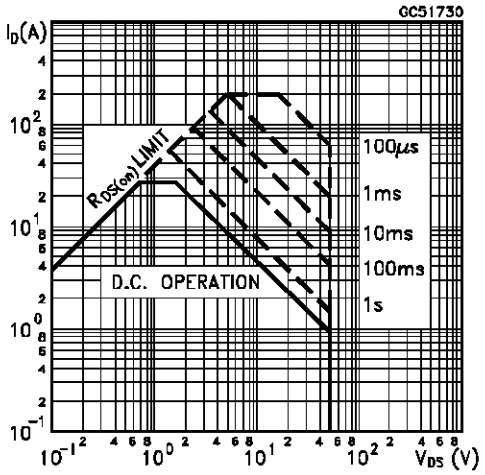
(*) Pulsed: Pulse duration = 300 µs, duty cycle 1.5 %

(•) Pulse width limited by safe operating area

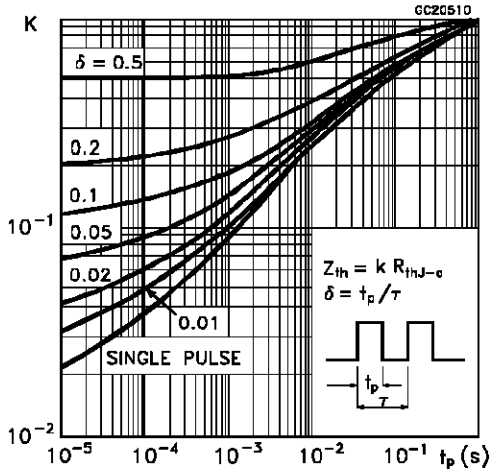
Safe Operating Area for TO-220



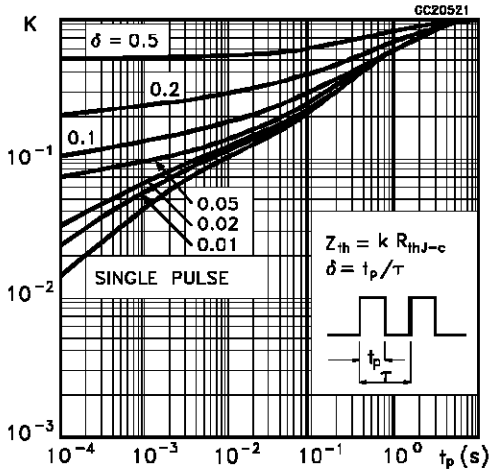
Safe Operating Area for ISOWATT220



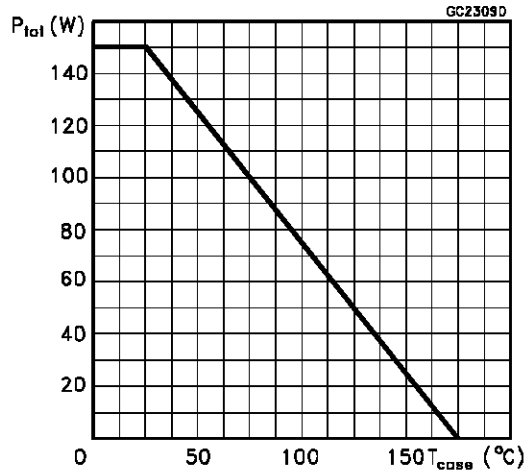
Thermal Impedance for TO-220



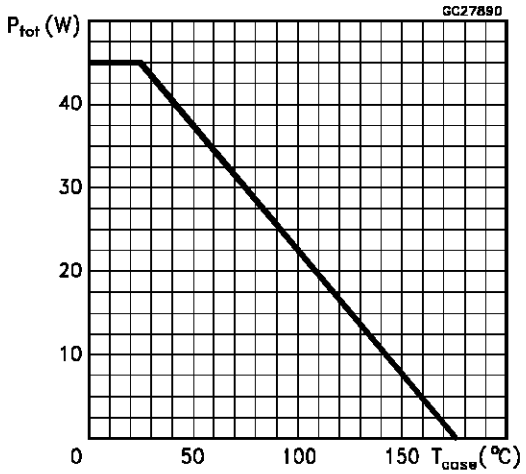
Thermal Impedance for ISOWATT220



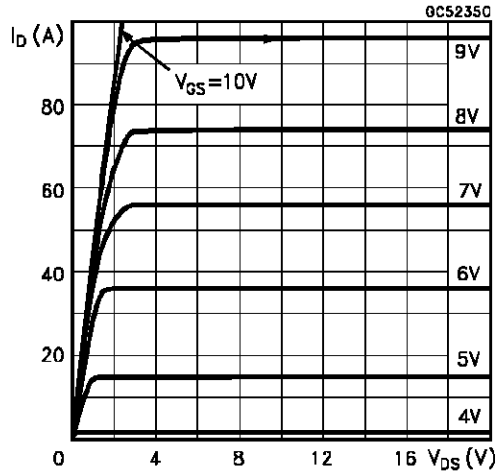
Derating Curve for TO-220



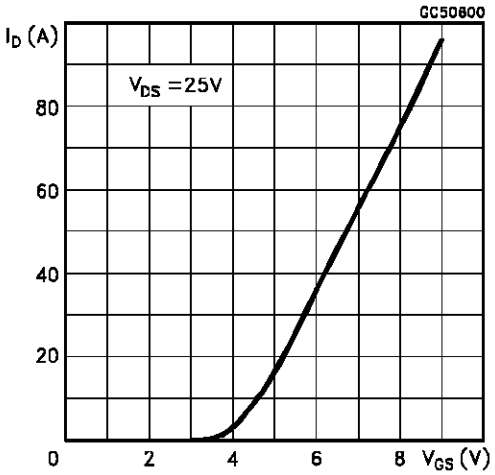
Derating Curve for ISOWATT220



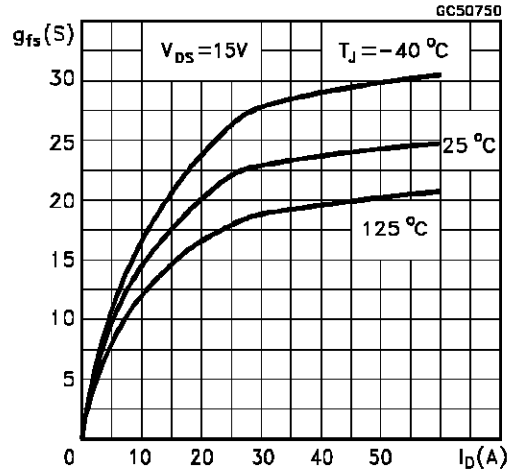
Output Characteristics



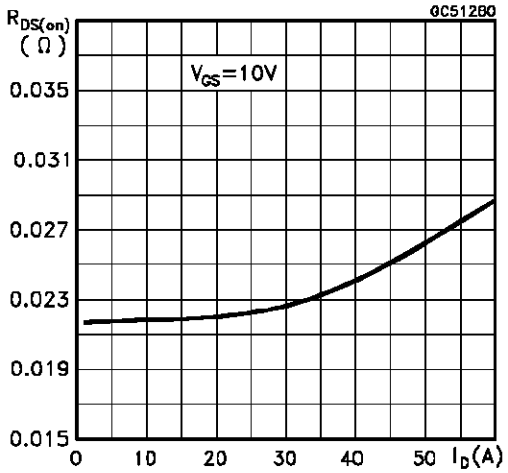
Transfer Characteristics



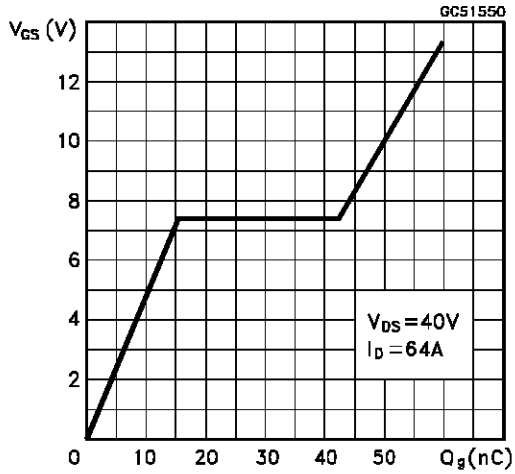
Transconductance



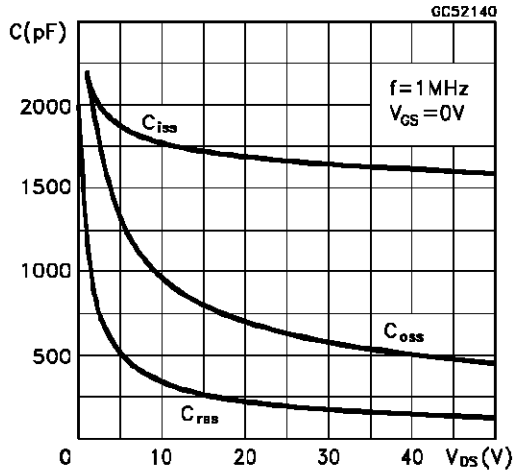
Static Drain-source On Resistance



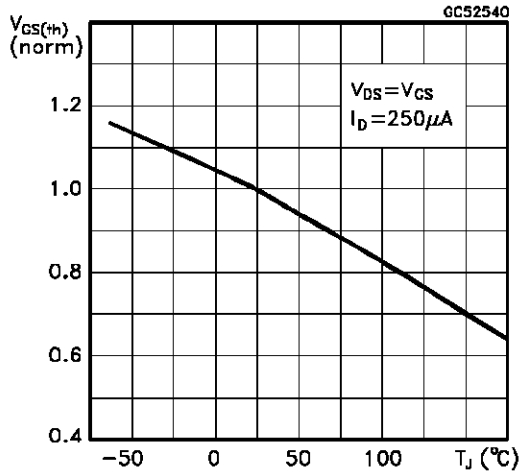
Gate Charge vs Gate-source Voltage



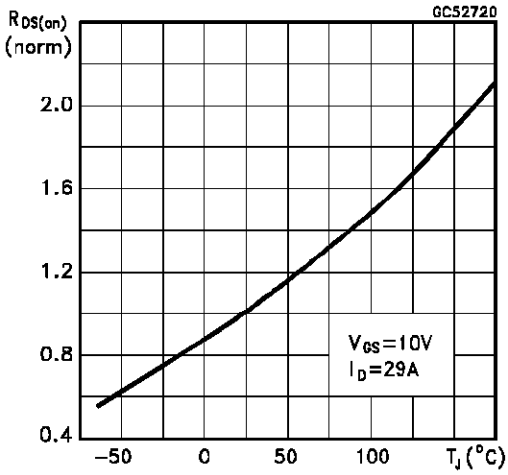
Capacitance Variations



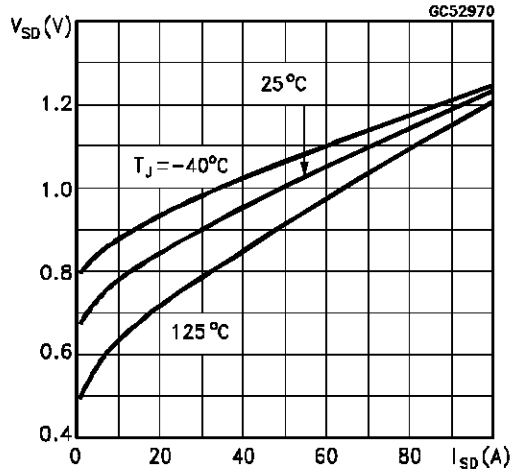
Normalized Gate Threshold Voltage vs Temperature



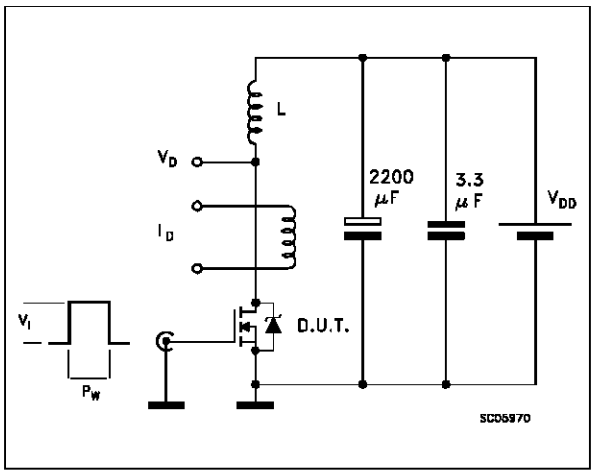
Normalized On Resistance vs Temperature



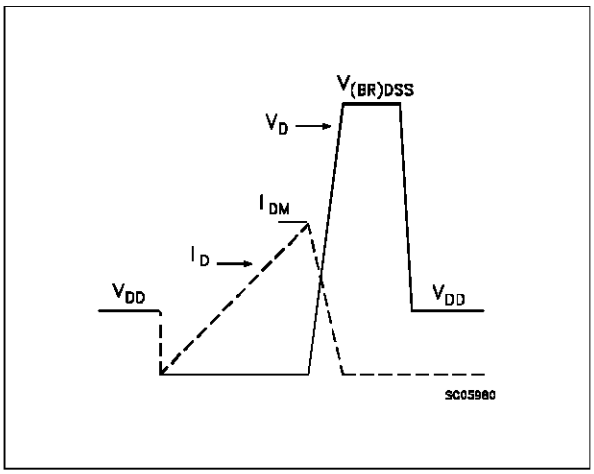
Source-drain Diode Forward Characteristics



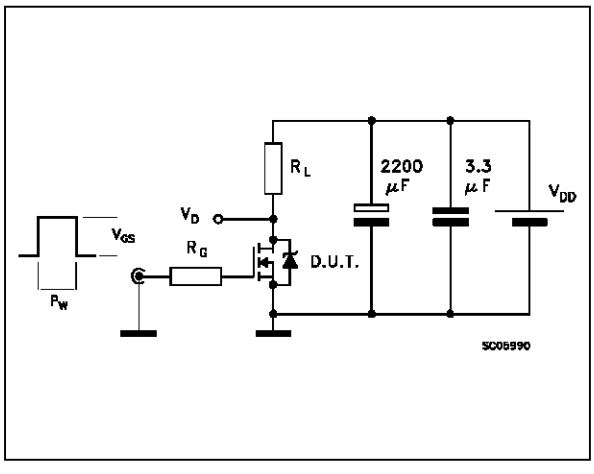
Unclamped Inductive Load Test Circuit



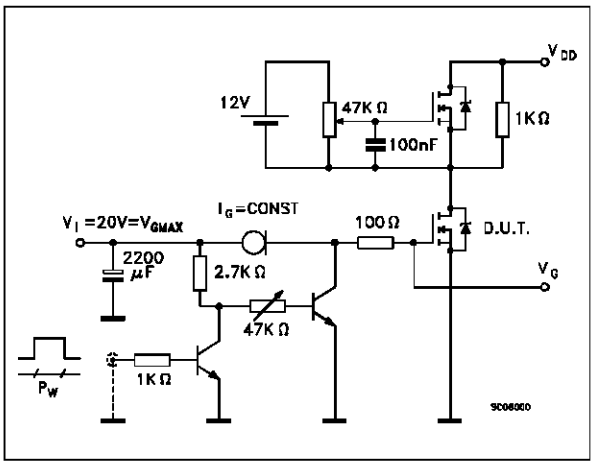
Unclamped Inductive Waveforms



Switching Time Test Circuit

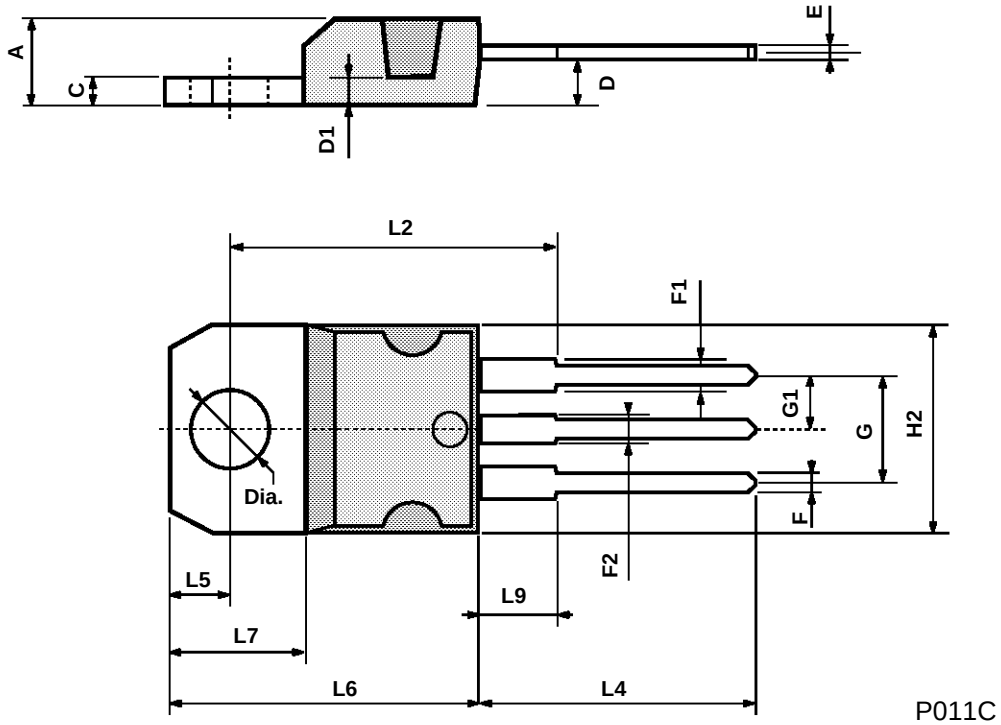


Gate Charge Test Circuit



TO-220 MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.40		4.60	0.173		0.181
C	1.23		1.32	0.048		0.051
D	2.40		2.72	0.094		0.107
D1		1.27			0.050	
E	0.49		0.70	0.019		0.027
F	0.61		0.88	0.024		0.034
F1	1.14		1.70	0.044		0.067
F2	1.14		1.70	0.044		0.067
G	4.95		5.15	0.194		0.203
G1	2.4		2.7	0.094		0.106
H2	10.0		10.40	0.393		0.409
L2		16.4			0.645	
L4	13.0		14.0	0.511		0.551
L5	2.65		2.95	0.104		0.116
L6	15.25		15.75	0.600		0.620
L7	6.2		6.6	0.244		0.260
L9	3.5		3.93	0.137		0.154
DIA.	3.75		3.85	0.147		0.151



P011C

ISOWATT220 MECHANICAL DATA

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A	4.4		4.6	0.173		0.181
B	2.5		2.7	0.098		0.106
D	2.5		2.75	0.098		0.108
E	0.4		0.7	0.015		0.027
F	0.75		1	0.030		0.039
F1	1.15		1.7	0.045		0.067
F2	1.15		1.7	0.045		0.067
G	4.95		5.2	0.195		0.204
G1	2.4		2.7	0.094		0.106
H	10		10.4	0.393		0.409
L2		16			0.630	
L3	28.6		30.6	1.126		1.204
L4	9.8		10.6	0.385		0.417
L6	15.9		16.4	0.626		0.645
L7	9		9.3	0.354		0.366
Ø	3		3.2	0.118		0.126