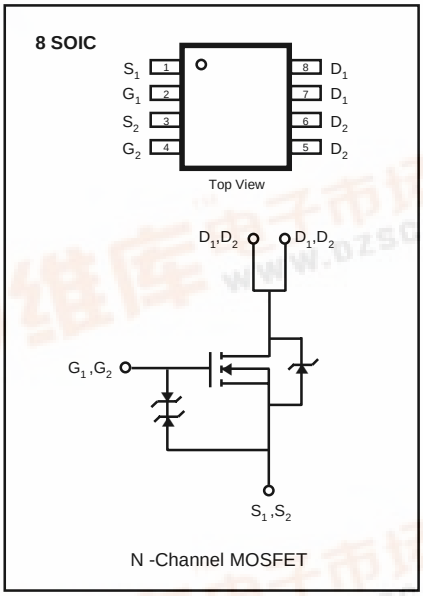
Dual N-CHANNEL POWER MOSFET SSD2025

FEATURES

- ❑ Lower $R_{DS(on)}$
- ❑ Improved Inductive Ruggedness
- ❑ Fast Switching Times
- ❑ Low Input Capacitance
- ❑ Extended Safe Operating Area
- ❑ Improved High Temperature Reliability



Product Summary

Part Number	BV_{DSS}	$R_{DS(on)}$	I_D
SSD2025	60V	0.10Ω	3.3A

Absolute Maximum Ratings

Symbol	Characteristic	Value	Units
V_{DSS}	Drain-to-Source Voltage	60	V
I_D	Continuous Drain Current T_A=25℃	3.3	A
	Continuous Drain Current T_A=70℃	2.6	
I_{DM}	Drain Current-Pulsed ①	10.0	A
V_{GS}	Gate-to-Source Voltage	±20	V
P_D	Total Power Dissipation (T_A=25℃)	2.0	W
	(T_A=70℃)	1.3	
T_J , T_{STG}	Operating and Junction Storage Temperature Range	- 55 to +150	℃

Thermal Resistance

Symbol	Characteristic	Typ.	Max.	Units
$R_{\theta JA}$	Junction-to-Ambient	--	62.5	℃/W

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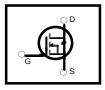
Rev. A1

Electrical Characteristics (T_C=25℃ unless otherwise specified)

Symbol	Characteristic	Min.	Typ.	Max.	Units	Test Condition
BV_{DSS}	Drain-Source Breakdown Voltage	60	--	--	V	V_{GS}=0V,I_D=250μA
$V_{GS(th)}$	Gate Threshold Voltage	1.0	--	--	V	V_{DS}= 5V ,I_D=250μA
I_{GSS}	Gate-Source Leakage , Forward	--	--	100	nA	V_{GS}=20V
	Gate-Source Leakage , Reverse	--	--	-100	nA	V_{GS}=-20V
I_{DSS}	Drain-to-Source Leakage Current	--	--	1.0	μA	V_{DS}=48V
		--	--	25		V_{DS}=48V,T_C=55℃
I_{DON}	On-State Drain-Source Current	10	--	--	A	V_{DS}=5V ,V_{GS}=10V
$R_{DS(on)}$	Static Drain-Source	--	0.065	0.1	Ω	V_{GS}=10V,I_D=3.3A
	On-State Resistance ②	--	0.084	0.2		V_{GS}=4.5V,I_D=2.5A
g_{FS}	Forward Transconductance ②	--	7.0	--	S	V_{DS} =15V,I_D=3.3A
$t_{d(on)}$	Turn-On Delay Time	--	16	25	ns	V_{DD}=30V,I_D=1.0A, R_0=6.0Ω, ②③
t_r	Rise Time	--	18	30		
$t_{d(off)}$	Turn-Off Delay Time	--	40	50		
t_f	Fall Time	--	23	40		
Q_g	Total Gate Charge	--	18	30	nC	V_{DS}=30V,V_{GS}=10V, I_D=3.3A ②③
Q_{gs}	Gate-Source Charge	--	2.3	--		
Q_{gd}	Gate-Drain (“Miller”) Charge	--	4.7	--		

Source-Drain Diode Ratings and Characteristics

Symbol	Characteristic	Min.	Typ.	Max.	Units	Test Condition
I_S	Continuous Source Current (Body Diode)	--	--	1.7	A	Modified MOSFET Symbol Showing the Integral Reverse P-N Junction Rectifier 
V_{SD}	Diode Forward Voltage ②	--	--	1.2	V	T_A=25℃,I_S=1.7A,V_{GS}=0V
t_{rr}	Reverse Recovery Time ②	--	70	100	ns	T_A=25℃,I_F=1.7A,di_F/dt=100A/μs

Notes ;

① Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature

② Pulse Test : Pulse Width = 250μs, Duty Cycle ≤ 2%

③ Essentially Independent of Operating Temperature

Fig 1. Output Characteristics

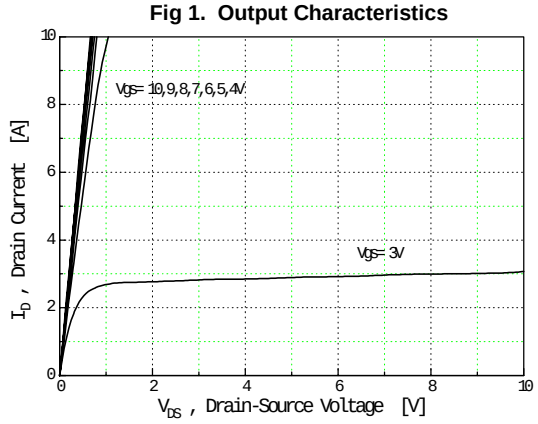


Fig 2. Transfer Characteristics

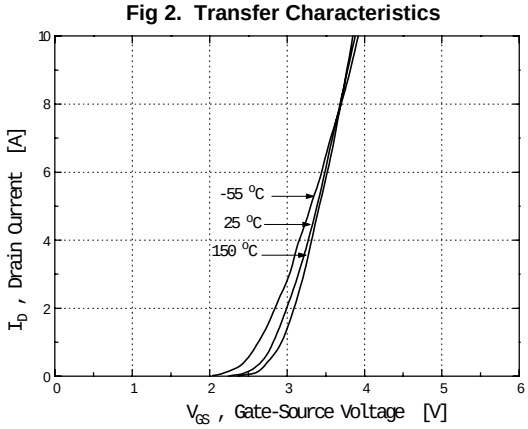


Fig 3. On-Resistance vs. Drain Current

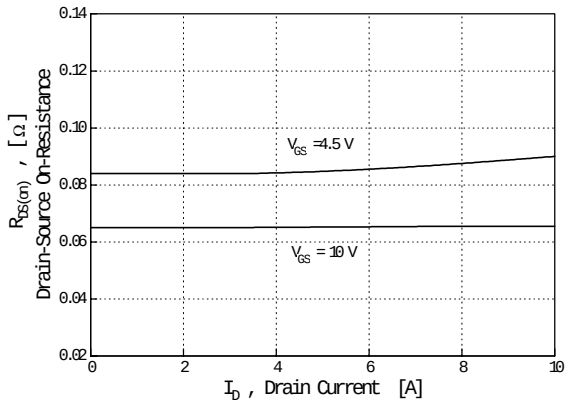


Fig 4. Source-Drain Forward Voltage

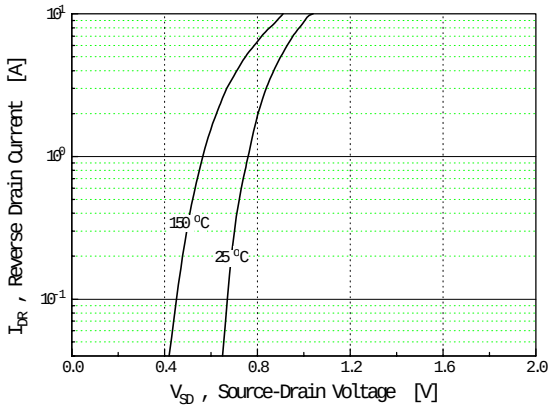


Fig 5. Capacitance vs. Drain-Source Voltage

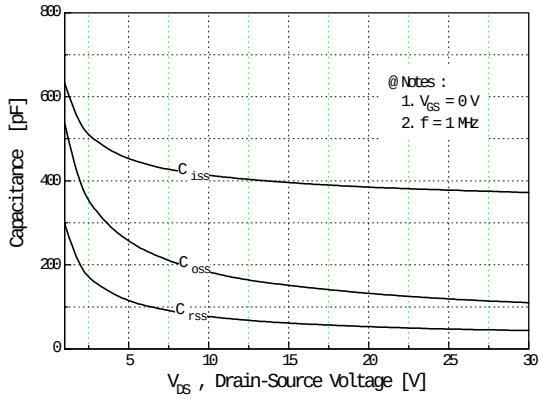


Fig 6. Gate Charge vs. Gate-Source Voltage

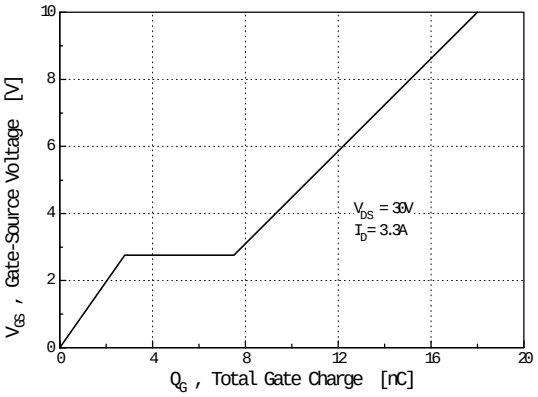


Fig 7. Breakdown Voltage vs. Temperature

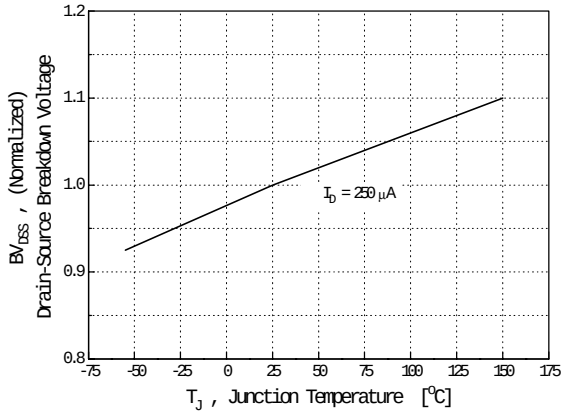


Fig 8. On-Resistance vs. Temperature

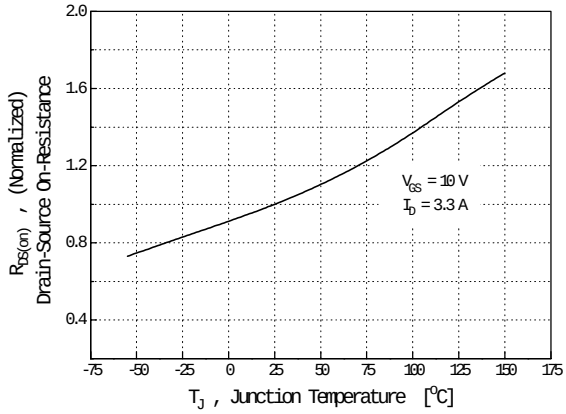
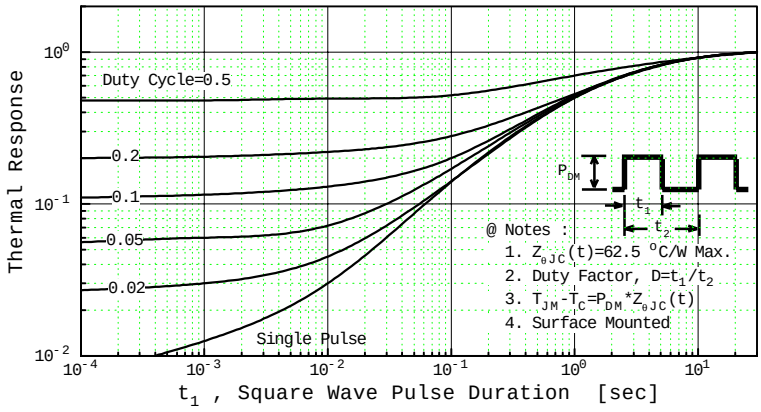


Fig 9. Nomalized Effective Transient Thermal Impedance, Junction-to-Ambient



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